



# STAC Performance Summit

November 19, 2009

Deutsche Bank, 23 Great Winchester Street, London

3:00 PM - Meeting starts

5:30 PM - Drinks in the ante room

7:30 PM - Doors close

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# AGENDA

WHAT	WHO
<b>Welcome</b>	<b>Jongi Mohiuddin</b> , Messaging Architect, Deutsche Bank <b>Peter Lankford</b> , Founder/Director, STAC
<b>Keynote address:</b> "The Race for Ultra-performance in the Algorithmic Era – An IBM Perspective"	<b>Philip Enness</b> , Banking and Financial Markets Global Solutions Manager, <a href="#">IBM</a> Sales and Distribution. Philip's experience in financial markets spans more than 27 years. He is a key contributor to IBM's Financial Markets strategy and has served IBM in EMEA, North America, China, and other areas of Asia Pacific. Prior to IBM, Philip spent 17 years at trading firms, in senior positions at Midland Bank (Midland Montagu) and Nikko Europe (Nikko Gilts).
<b>STAC Update</b>	<b>Jack Gidding</b> , Associate Director, STAC
<b>Innovation Roundup – Part 1</b> <ul style="list-style-type: none"> <li>• "Continuous Intelligence: Making Sense of a Fast-Moving World"</li> <li>• "Winning in the Market with the Next Generation Messaging Infrastructure"</li> <li>• "The era of unified messaging: Hardware, better, faster, stronger"</li> </ul>	<i>Each Innovation Roundup presenter has 5 minutes.</i> <b>Dale Stevens</b> , Sales Director, EMEA, <a href="#">Aleri</a> <b>Lalit Nathwani</b> , Business Development Director, <a href="#">29West</a> <b>Dave Clare</b> , Director, EMEA Sales, <a href="#">Solace Systems</a>
<b>Standardized Benchmarks for Trading Systems</b> STAC will review the Council's work on benchmark specifications across market data, analytics, and trade execution	<b>Peter Lankford</b> , Founder/Director, STAC
<b>Innovation Roundup – Part 2</b> <ul style="list-style-type: none"> <li>• "HP Low-Latency Updates: STAC HP Benchmark Lab, Real-Time Linux, and Layer 2 Fabrics"</li> <li>• "Using hardware technologies to accelerate and cut costs in your messaging infrastructure"</li> <li>• "Ultra Low Latency without the Pain"</li> </ul>	<i>Each Innovation Roundup presenter has 5 minutes.</i> <b>Jonathan Traer-Clark</b> , Global Director, Capital Markets, <a href="#">HP</a> <b>John Page</b> , Director of Business Development, Messaging Products, <a href="#">TIBCO Software</a> <b>Dr. David Riddoch</b> , Chief Software Architect, <a href="#">Solarflare</a>
<b>Running STAC-M2 with IBM</b> <ul style="list-style-type: none"> <li>• Explain the STAC-M2 specs and harness (Lankford)</li> <li>• Review results from STAC-M2 tests of IBM WebSphere Low Latency Messaging (LLM) on Voltaire/Mellanox QDR InfiniBand and Intel Nehalem-based IBM blades (Lankford)</li> <li>• Discuss the process of running STAC-M2 from the practitioner's perspective (Bruinsma)</li> </ul>	<b>Falke Bruinsma</b> , Chief Architect, WFO/LLM, <a href="#">IBM</a> <b>Peter Lankford</b> , Founder/Director, STAC
<b>WebSphere MQ Low Latency Messaging</b> IBM will provide product details and case studies on LLM.	<b>Folu Okunseinde</b> , Solutions Architect, WFO/LLM, <a href="#">IBM</a>
<b>Cocktails</b>	<b>Everyone</b>