

# **PUMPING PYTHON PERFORMANCE**

Sergey Maidanov

Software Engineering Manager,

Scripting Analyzers & Tools, Intel

# Programming Languages by Popularity

### **Python** remains **#1** programming language in **hiring demand** followed by **Java** and **C++**

### **Go** and **Scala** demonstrate **strong growth** for last 2 years

Most Popular Coding Languages of 2015



#### **Optimization Notice**

Copyright © 2015, Intel Corporation. All rights reserved. \*Other names and brands may be claimed as the property of others. STAC Summit, November 2015

\* Source: CodeEval, Feb 2015



### **Programming Languages Productivity**

### LANGUAGE VERBOSITY (LOC/FEATURE)

PROGRAMMING COMPLEXITY (HOURS)



#### **Optimization Notice**

Copyright © 2015, Intel Corporation. All rights reserved. \*Other names and brands may be claimed as the property of others. STAC Summit, November 2015

\* LPrechelt, An empirical comparison of seven programming languages, IEEE Computer, 2000, Vol. 33, Issue 10, pp. 23-29 \* RedMonk - D.Berkholz, Programming languages ranked by expressiveness



# Numerical Modeling: From Prototype To Production



#### **Optimization Notice**

Copyright © 2015, Intel Corporation. All rights reserved. \*Other names and brands may be claimed as the property of others. STAC Summit, November 2015

# Why's Interpreted Code Unfriendly To Modern HW?

### Moore's law still works and will work for at least next 10 years

### We have hit limits in

- Power
- Instruction level parallelism
- Clock speed

### But not in

- Transistors (more memory, bigger caches, wider SIMD, specialized HW)
- Number of cores

### Flop/Byte continues growing

• 10x worse in last 20 years

### Efficient software development means

- Optimizations for data locality & contiguity
- Vectorization
- Threading



#### **Optimization Notice**

Copyright © 2015, Intel Corporation. All rights reserved. \*Other names and brands may be claimed as the property of others STAC Summit, November 2015

Source: J.Cownie, HPC Trends and What They Mean for Me, Imperial College, Oct. 2015





Configuration info: - Versions: Intel® Distribution for Python 2.7.10 Technical Preview 1 (Aug 03, 2015), icc 15.0; Hardware: Intel® Xeon® CPU E5-2698 v3 @ 2.30GHz (2 sockets, 16 cores each, HT=OFF), 64 GB of RAM, 8 DIMMS of 8GB@2133MHz; Operating System: Ubuntu 14.04 LTS.

#### **Optimization Notice**

Copyright © 2015, Intel Corporation. All rights reserved. \*Other names and brands may be claimed as the property of others. STAC Summit, November 2015

### Possible Improvement Approaches

How to make Python more usable in both prototyping & production?

- Numerical/machine learning packages (NumPy/SciPy/Scikit-learn) accelerated with native libraries (e.g. Intel<sup>®</sup> MKL)
- Python language extensions that exploit vectorization and multicore parallelism, e.g. Cython (via GCC/ICC), Numba (LLVM)
- Better performance profiling of Python codes
- Packages and extensions for multi-node parallelism, e.g. mpi4py
- Integration with Big Data/ML infrastructures (Hadoop, Spark)

#### **Optimization Notice**

Copyright © 2015, Intel Corporation. All rights reserved. \*Other names and brands may be claimed as the property of others.





Configuration info: - Versions: Intel® Distribution for Python 2.7.10 Technical Preview 1 (Aug 03, 2015), Ubuntu\* built Python\*: Python 2.7.10, NumPy 1.9.2 built with gcc 4.8.4; Hardware: Intel® Xeon® CPU E5-2698 v3 @ 2.30GHz (2 sockets, 16 cores each, HT=OFF), 64 GB of RAM, 8 DIMMS of 8GB@2133MHz; Operating System: Ubuntu 14.04 LTS.

#### Optimization Notice

Copyright © 2015, Intel Corporation. All rights reserved. \*Other names and brands may be claimed as the property of others. STAC Summit, November 2015



# Python Language Extensions For Performance



Configuration info: - Versions: Intel® Distribution for Python 2.7.10 Technical Preview 1 (Aug 03, 2015), Ubuntu\* built Python\*: Python 2.7.10, NumPy 1.9.2 built with gcc 4.8.4, llvm 3.6.0, Numba 0.22.0, icc 15.0; Hardware: Intel® Xeon® CPU E5-2698 v3 @ 2.30GHz (2 sockets, 16 cores each, HT=OFF), 64 GB of RAM, 8 DIMMS of 8GB@2133MHz; Operating System: Ubuntu 14.04 LTS.

#### Optimization Notice

Copyright © 2015, Intel Corporation. All rights reserved. \*Other names and brands may be claimed as the property of others. STAC Summit, November 2015



#### Optimization Notice

Copyright © 2015, Intel Corporation. All rights reserved. \*Other names and brands may be claimed as the property of others. STAC Summit, November 2015

10

### **Big Data Requires End-To-End Solutions**

### What device do I run analytics?

- Perform analysis close to data source to optimize response latency, decrease network bandwidth utilization, and maximize security.
- Offload data to cluster for large-scale analytics only.
- Make personalized decisions on a client device



#### **Optimization Notice**

Copyright © 2015, Intel Corporation. All rights reserved. \*Other names and brands may be claimed as the property of others.

# **Big Data Flow and Computational Flow**



Capacity	Big Data Attributes		Computational Solution	
	Distributed across different nodes/devices		•Distributed computing, e.g. comm-avoiding algorithms	
	Huge data size not fitting into device memory		•Distributed computing •Streaming algorithms	
	Data coming in time		•Data buffering •Streaming algorithms	
	Non-homogeneous data		<ul> <li>Categorical→Numeric (counters, histograms, etc)</li> <li>Homogeneous numeric data kernels</li> <li>Conversions, Indexing, Repacking</li> </ul>	
	Sparse/Missing/Noisy data		<ul> <li>Sparse data algorithms</li> <li>Recovery methods (bootstrapping, outlier correction)</li> </ul>	
reaming Computing Offline Computing		Offline Computing	Converts, Indexing, Repacking	Data Recovery
D <sub>2</sub> D	S <sub>i</sub> ,R <sub>i</sub>	$D_k \rightarrow D_k \cdots D_1 \rightarrow V - R$	1 F F F F F 2 D D D D D 3 B B B 4 D D D D 4 D D D D 4 D D D D 5 C C C C C 6 C C C C C 7 F F F F 8 C C C C C	1       F
$S_{i+1} = T(S_i, D_i)$ $R_{i+1} = F(S_{i+1})$ $R = F(D_1,, D_k)$		$R=F(D_1,\ldots,D_k)$	Histogram	

#### **Optimization Notice**

R<sub>ν</sub>

D

Copyright © 2015, Intel Corporation. All rights reserved. \*Other names and brands may be claimed as the property of others.

 $D_3 D_2$ 

STAC Summit, November 2015

# Intel<sup>®</sup> DAAL – Essentials for End-To-End Analytics

- C++ and Java\*/Scala\* library for data analytics
  - Targeting Python and R interfaces in future releases
  - "MKL" for machine learning with a few key differences
    - Optimizes entire data flow vs. compute part only
    - Targets both data center and edge devices
  - Supports offline, **online** and **distributed** data processing
  - Abstracted from cross-device communication layer
    - Allows plugging in different Big Data & IoT analytics frameworks
    - Comes with samples for Hadoop\*, Spark\*, MPI\*
- Builds upon MKL/IPP for best performance



#### Apriori on Intel<sup>®</sup> Xeon<sup>®</sup> Processor E5-2699 v3



#### Configuration Info - Versions: Intel<sup>®</sup> Data Analytics Acceleration Library 2016, CDH v5.3.1, Apache Spark<sup>\*</sup> v1.2.0, Weka 3.6.12; Hardware: Intel<sup>®</sup> Xeon<sup>®</sup> Processor E5-2699 v3, 2 Eighteen-core CPUs (45MB LLC, 2.3GHz), 128GB of RAM per node: Operating System: CentOS 6.6 x86 64.

Optimization Notice

Copyright © 2015, Intel Corporation. All rights reserved. \*Other names and brands may be claimed as the property of others. STAC Summit, November 2015

# Summary

- Python is among top productivity languages
- Python is unfriendly to modern hardware, and hence to production use
- New tools and libraries allow making tradeoff between productivity and performance
- Intel is investing in Python to be more usable in prototyping and production
  - Intel<sup>®</sup> Distribution for Python in Tech Preview Now!
  - Intel<sup>®</sup> VTune Amplifier for Python available for evaluation Now!
- Big Data analytics requires end-to-end solutions
- Intel has "end-to-end" response for new analytics challenges
  - Intel<sup>®</sup> Data Analytics Acceleration Library 2016 product available Now!

#### **Optimization Notice**

Copyright © 2015, Intel Corporation. All rights reserved. \*Other names and brands may be claimed as the property of others. Intel Confidential – NDA presentation; presentation contains Intel confidential information presented to customers with a non-disclosure agreement



# Legal Disclaimer & Optimization Notice

INFORMATION IN THIS DOCUMENT IS PROVIDED "AS IS". NO LICENSE, EXPRESS OR IMPLIED, BY ESTOPPEL OR OTHERWISE, TO ANY INTELLECTUAL PROPERTY RIGHTS IS GRANTED BY THIS DOCUMENT. INTEL ASSUMES NO LIABILITY WHATSOEVER AND INTEL DISCLAIMS ANY EXPRESS OR IMPLIED WARRANTY, RELATING TO THIS INFORMATION INCLUDING LIABILITY OR WARRANTIES RELATING TO FITNESS FOR A PARTICULAR PURPOSE, MERCHANTABILITY, OR INFRINGEMENT OF ANY PATENT, COPYRIGHT OR OTHER INTELLECTUAL PROPERTY RIGHT.

Software and workloads used in performance tests may have been optimized for performance only on Intel microprocessors. Performance tests, such as SYSmark and MobileMark, are measured using specific computer systems, components, software, operations and functions. Any change to any of those factors may cause the results to vary. You should consult other information and performance tests to assist you in fully evaluating your contemplated purchases, including the performance of that product when combined with other products.

Copyright © 2015, Intel Corporation. All rights reserved. Intel, Pentium, Xeon, Xeon Phi, Core, VTune, Cilk, and the Intel logo are trademarks of Intel Corporation in the U.S. and other countries.

#### **Optimization Notice**

Intel's compilers may or may not optimize to the same degree for non-Intel microprocessors for optimizations that are not unique to Intel microprocessors. These optimizations include SSE2, SSE3, and SSSE3 instruction sets and other optimizations. Intel does not guarantee the availability, functionality, or effectiveness of any optimization on microprocessors not manufactured by Intel. Microprocessor-dependent optimizations in this product are intended for use with Intel microprocessors. Certain optimizations not specific to Intel microarchitecture are reserved for Intel microprocessors. Please refer to the applicable product User and Reference Guides for more information regarding the specific instruction sets covered by this notice.

Notice revision #20110804



