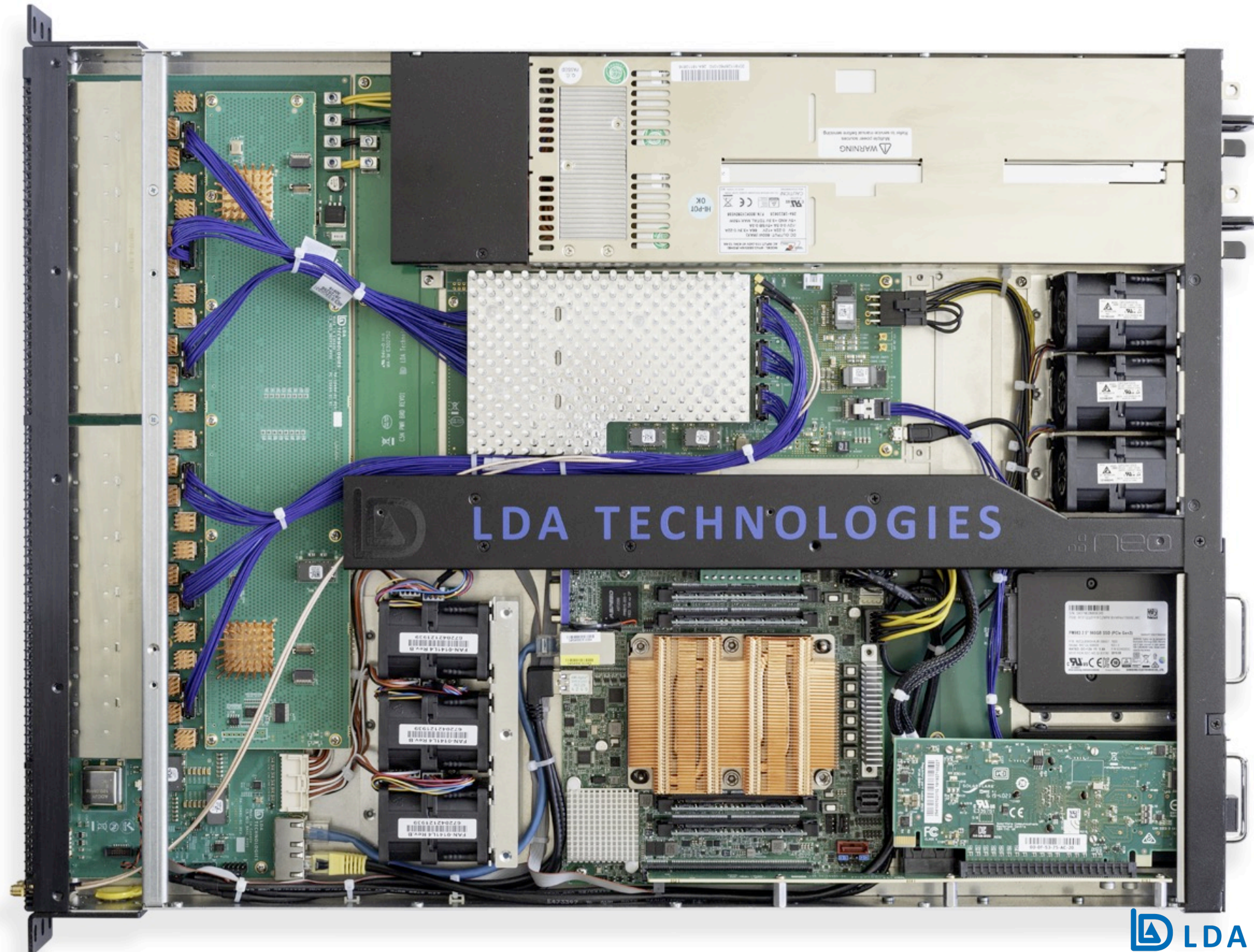




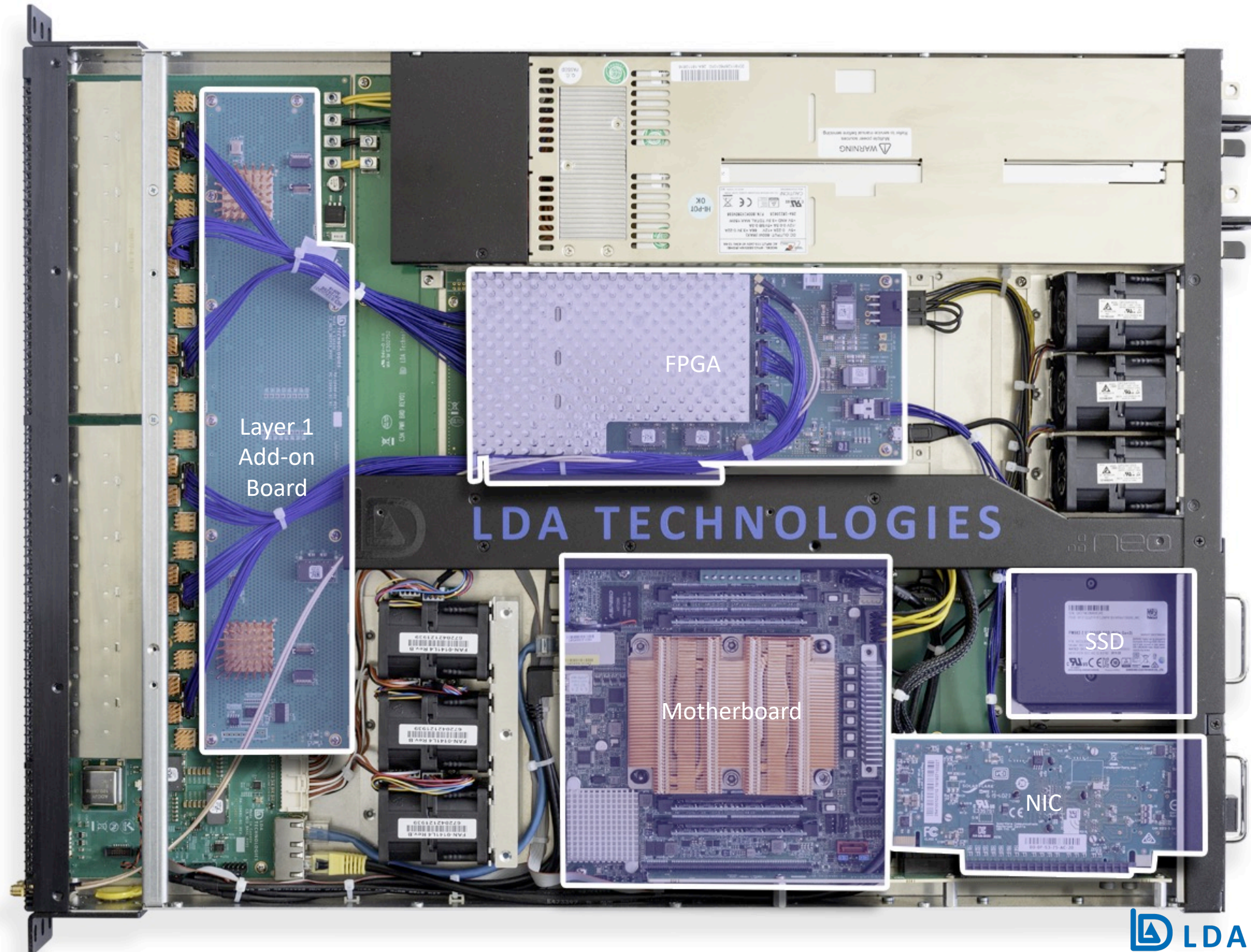
Where functionality meets ultra-low latency



A LOOK INSIDE

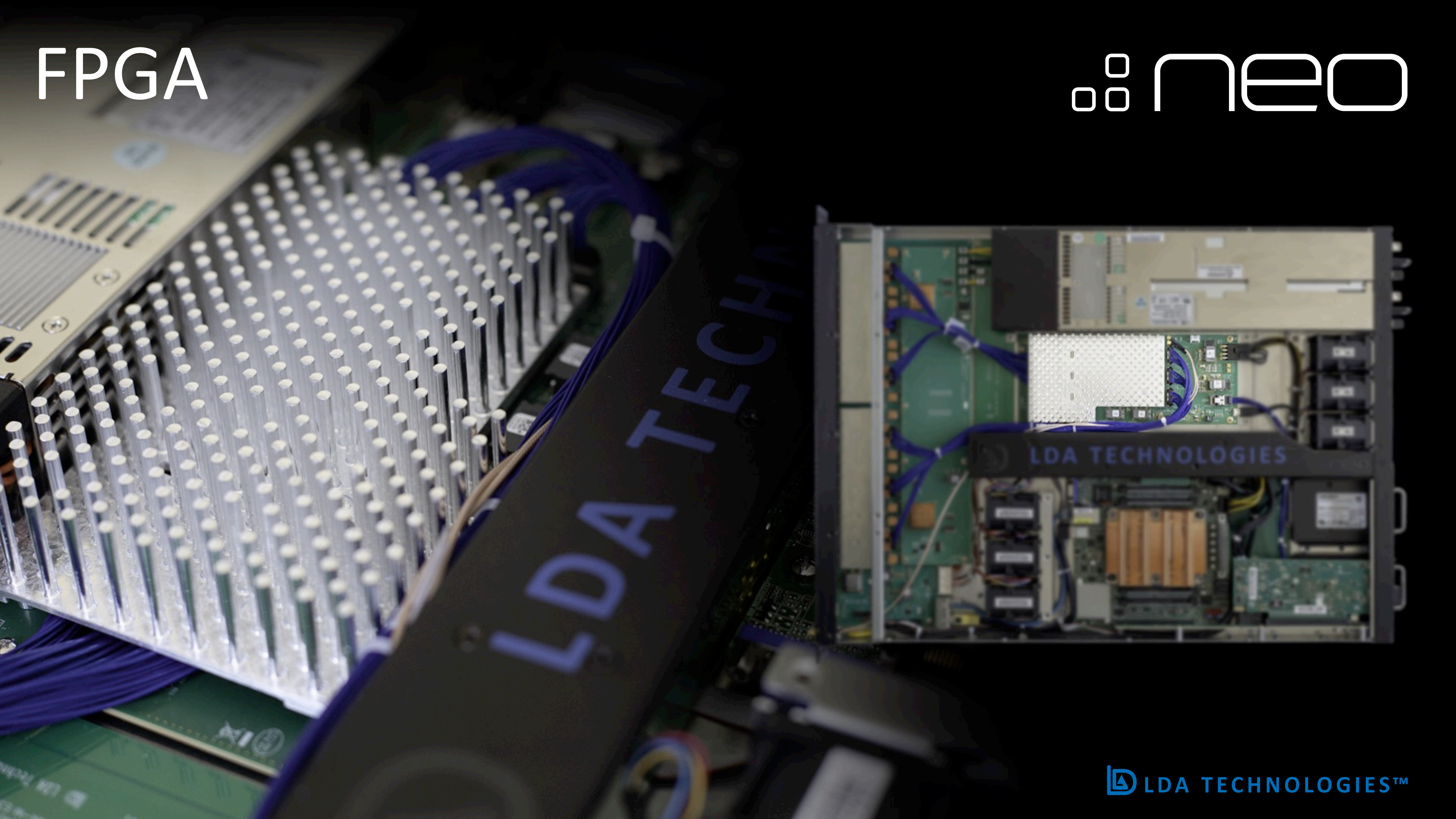


A LOOK INSIDE



FPGA

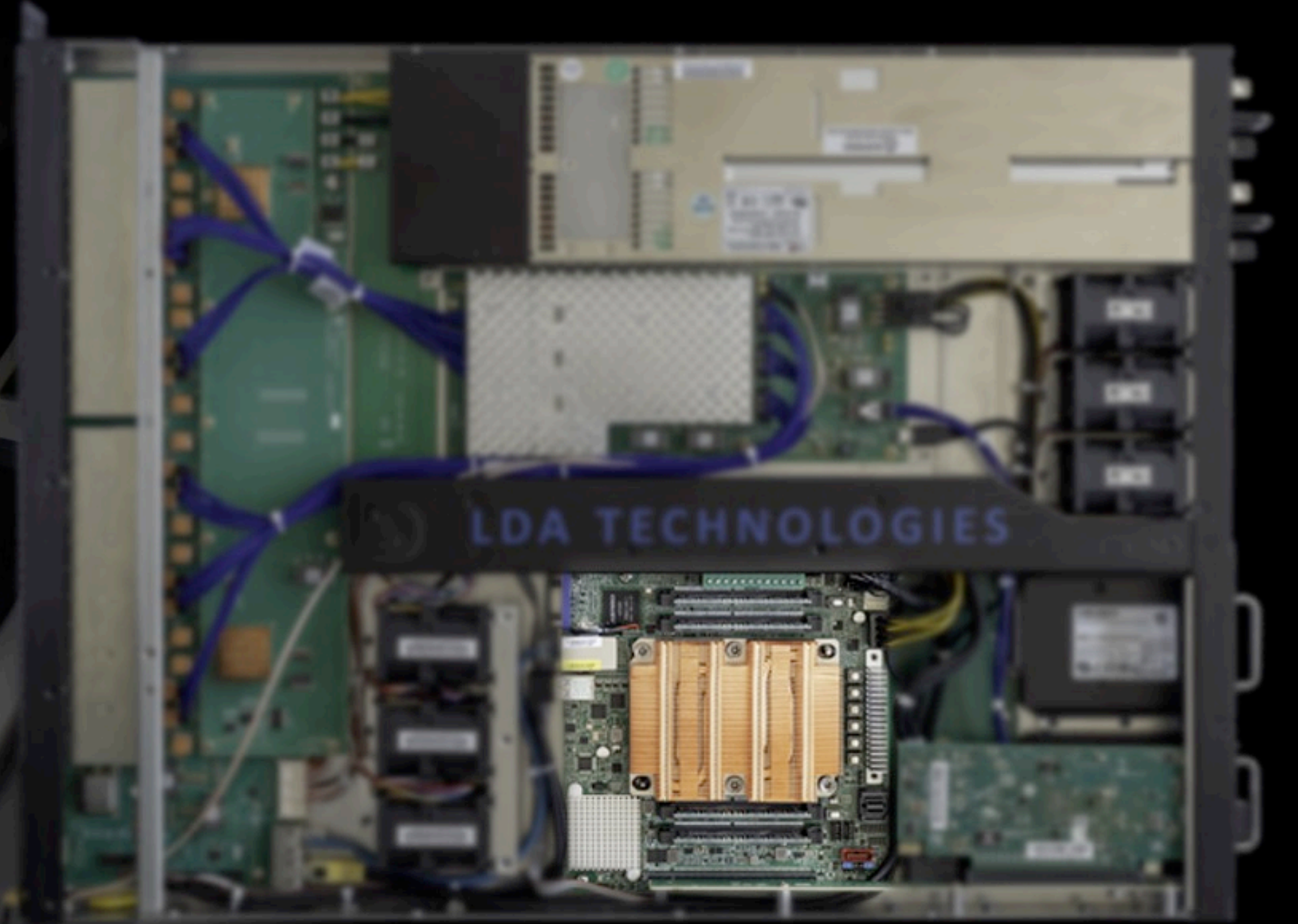
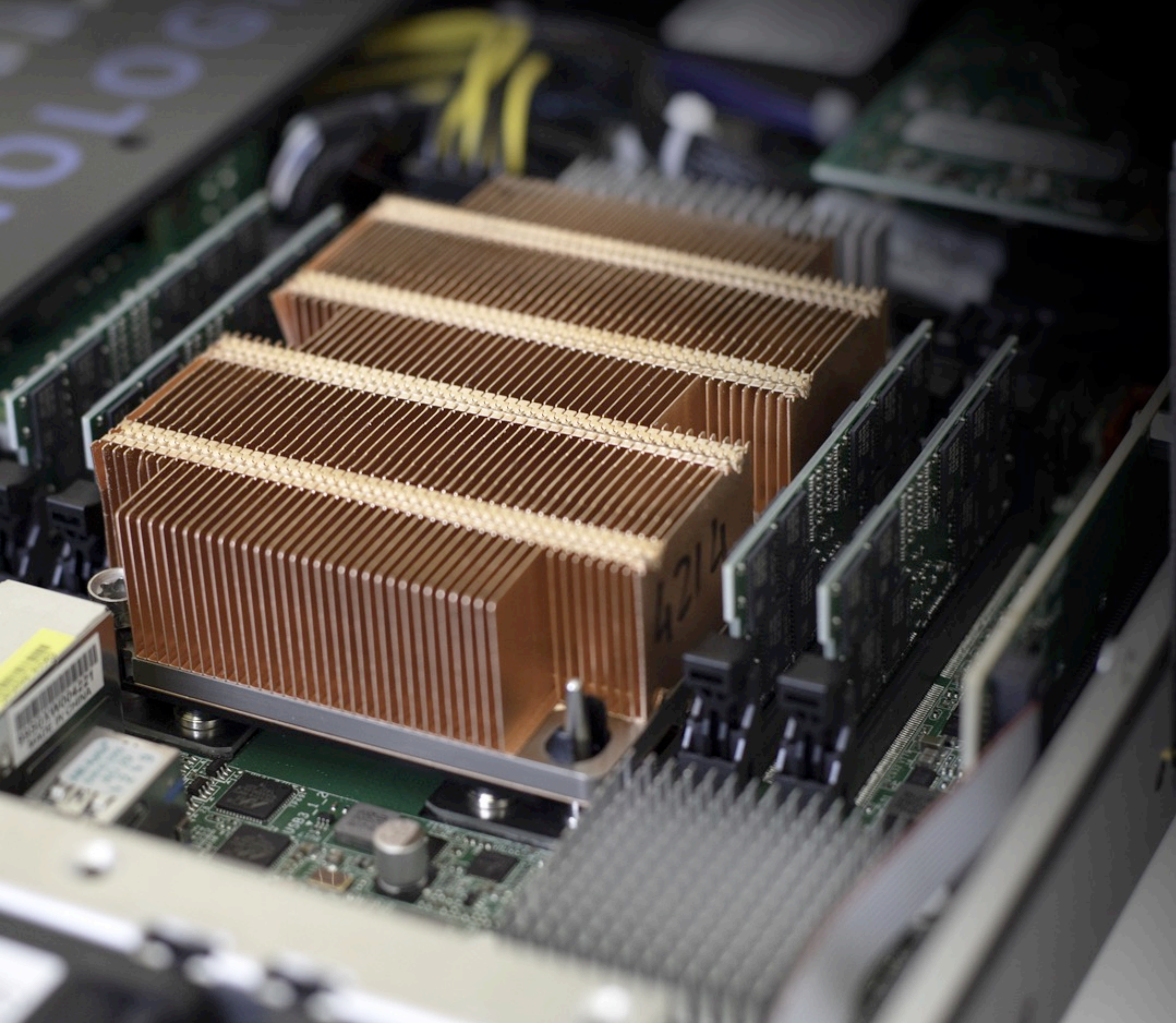
NEO



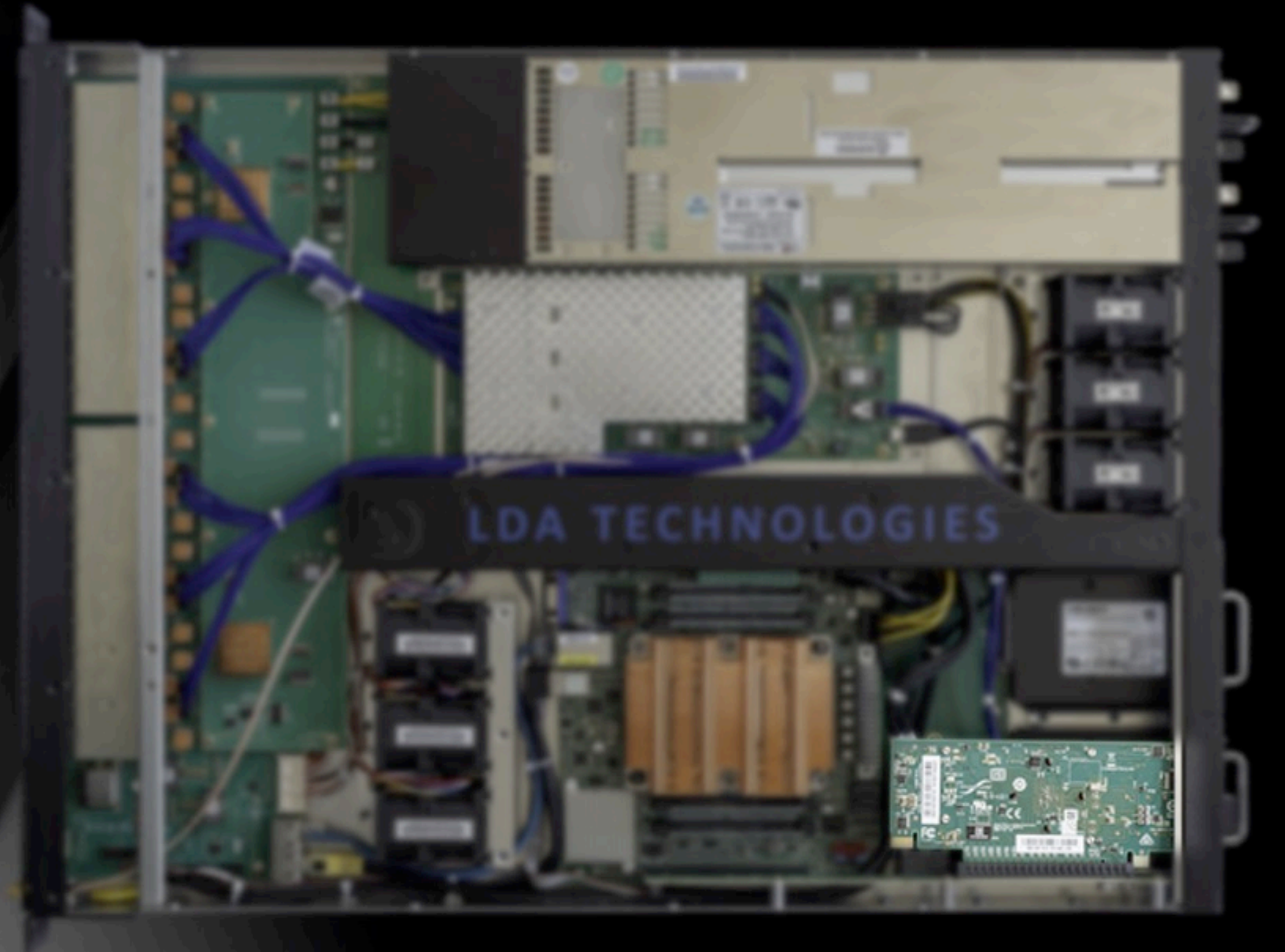
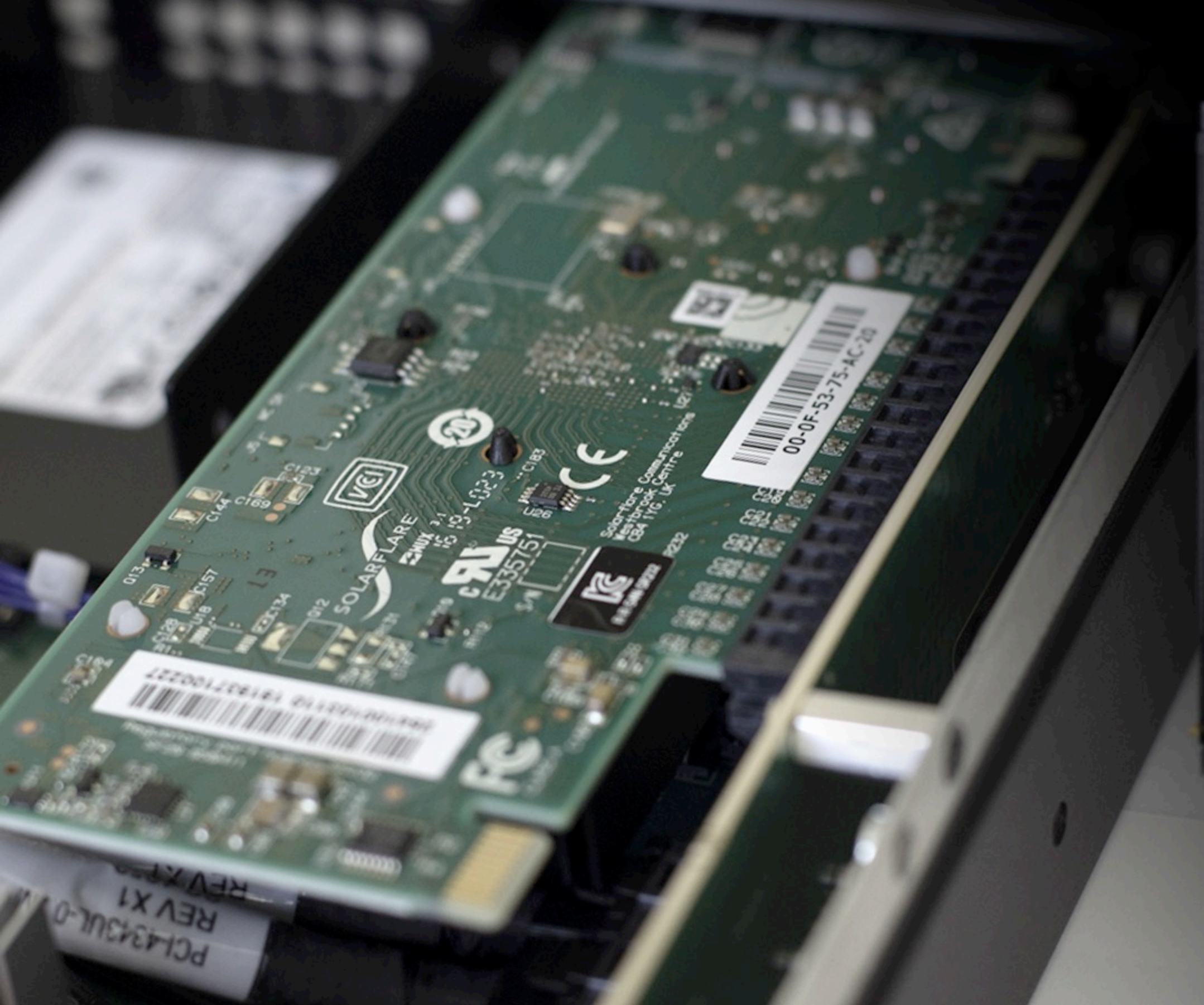
LDA TECHNOLOGIES

LDA TECHNOLOGIES

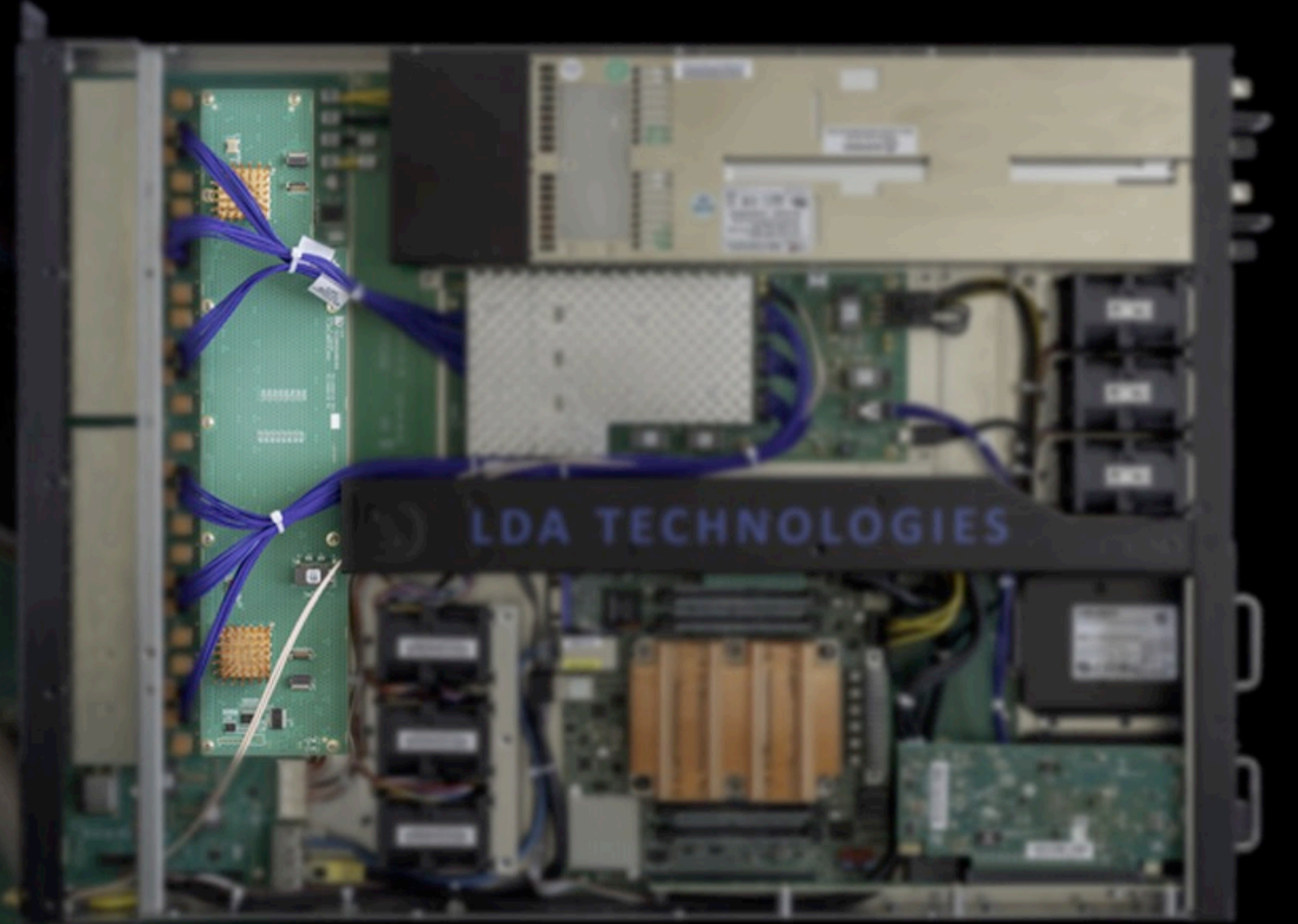
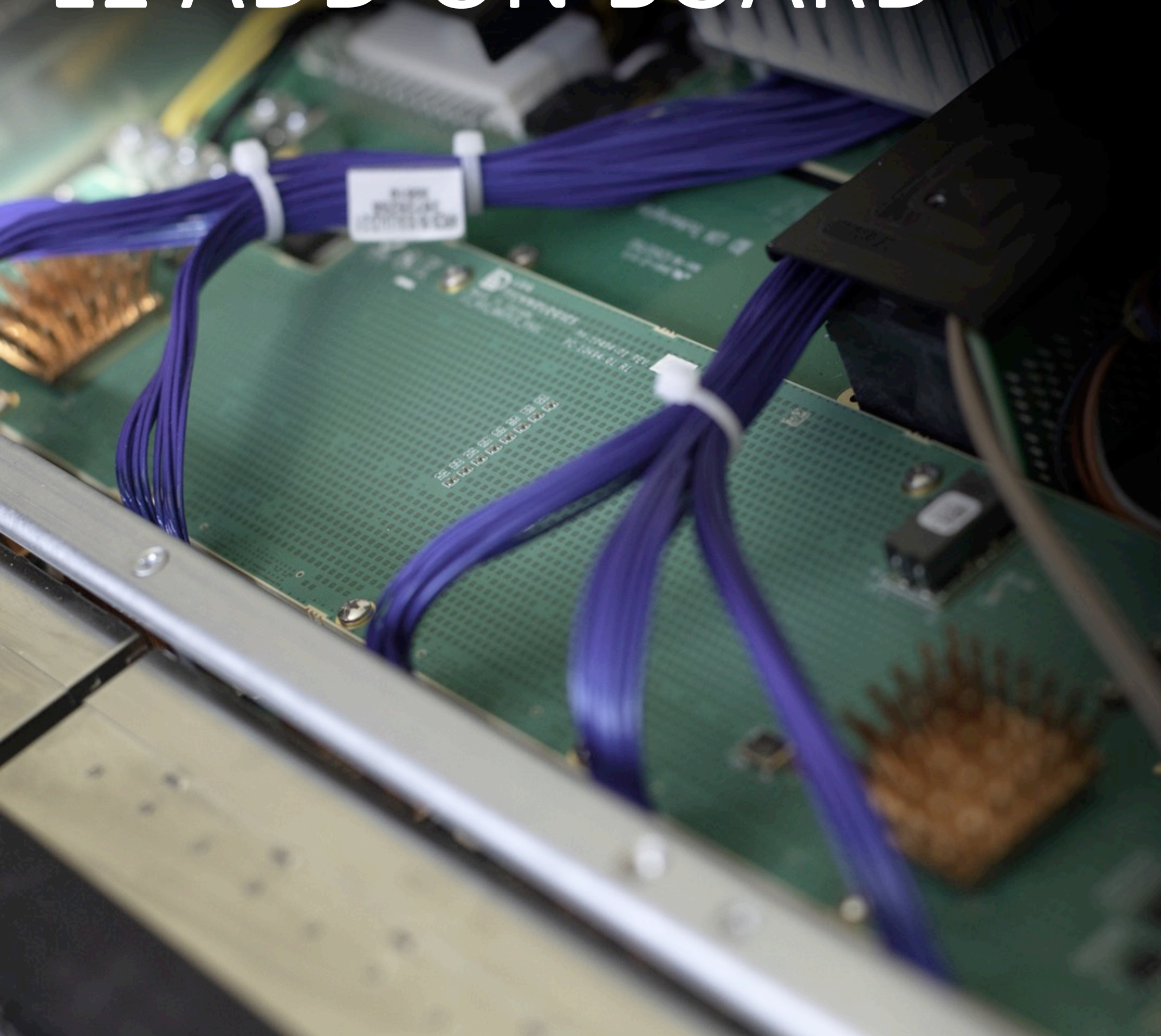
MOTHERBOARD



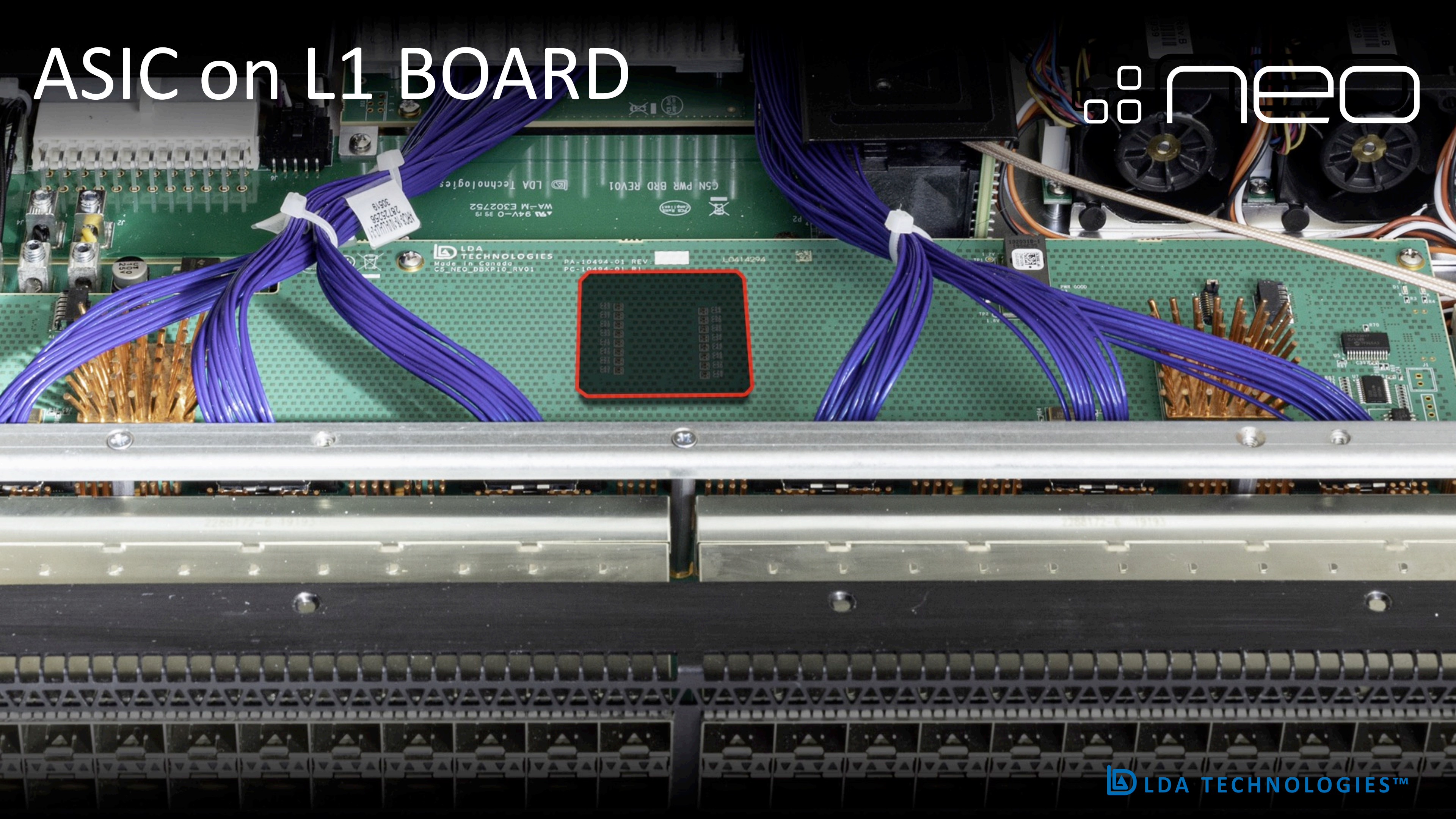
NETWORK INTERFACE CARD



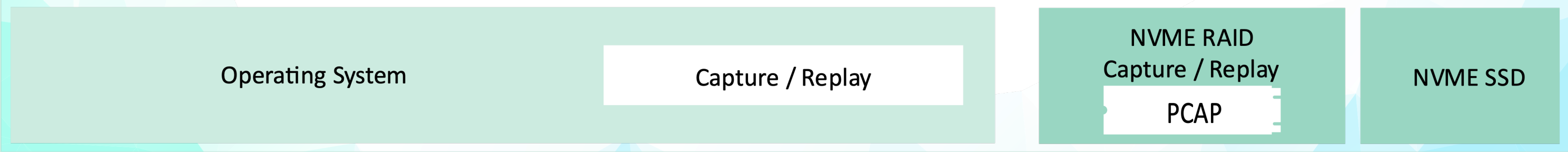
L1 ADD-ON BOARD



ASIC on L1 BOARD



NET-RECAP



PCIe x16

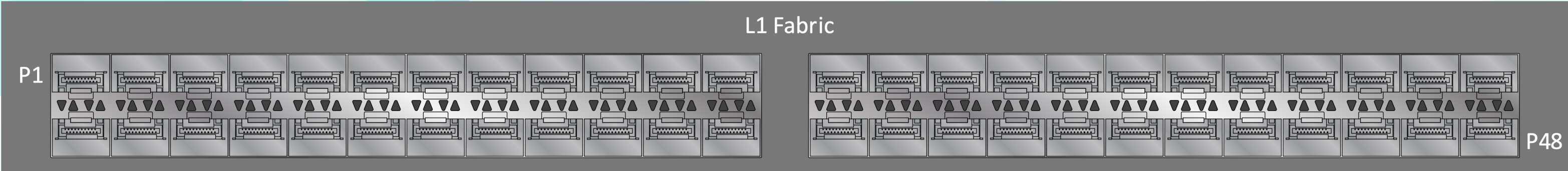


100 G



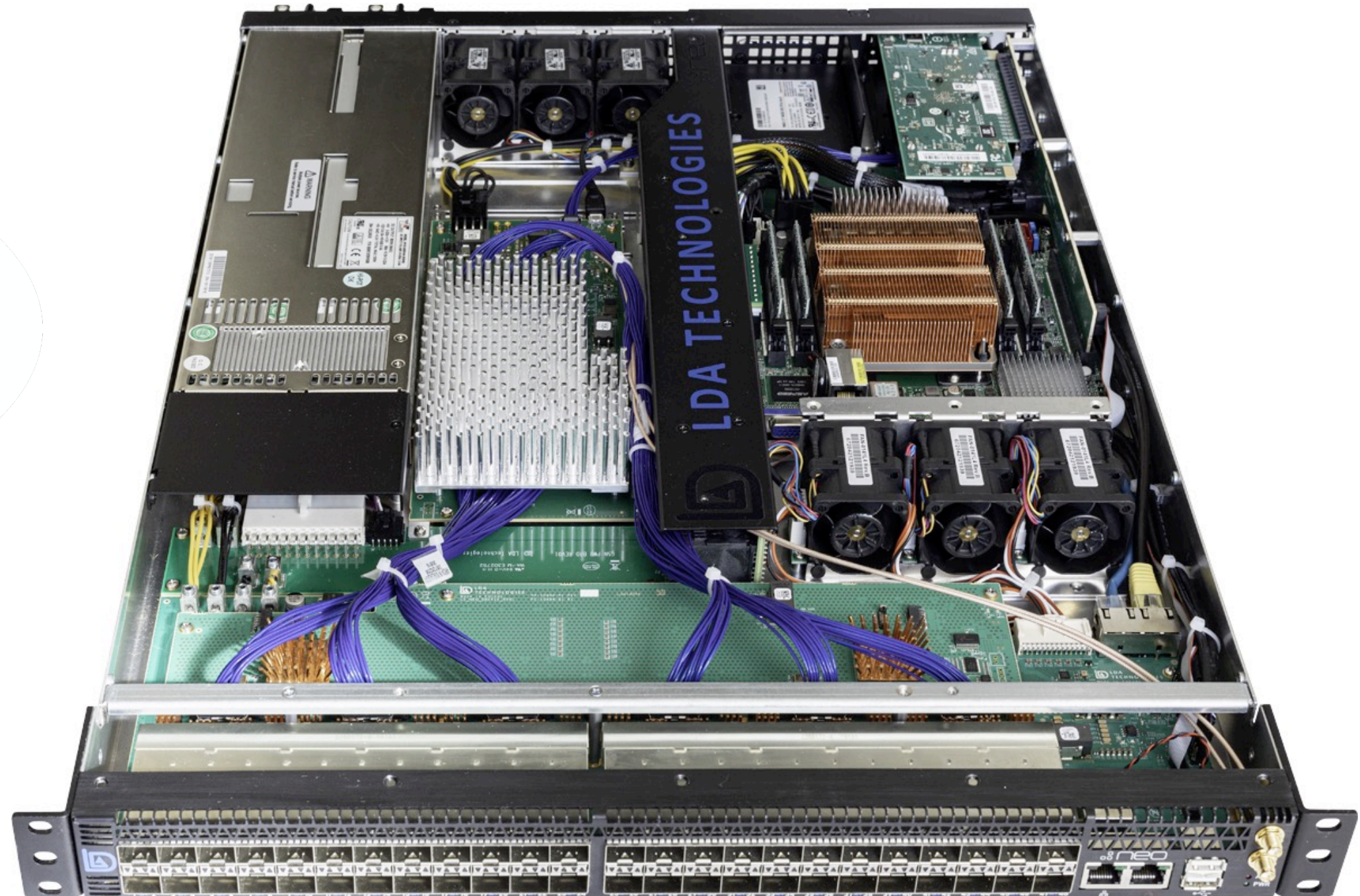
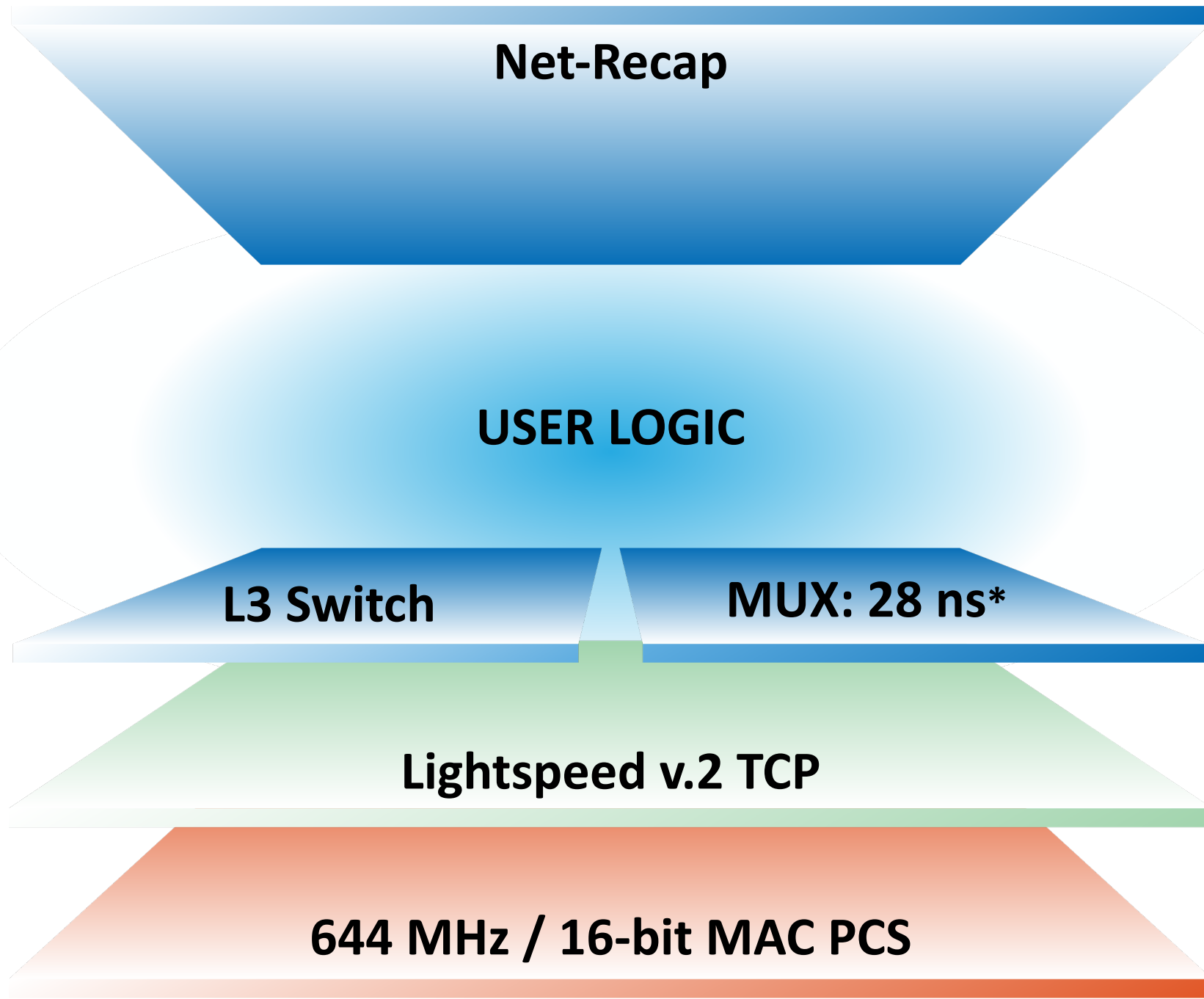
10 G or 25 G

L1 Fabric



Ports can be configured for 10 G or 25 G Ethernet in sets of 16

IP CORES



*Not STAC Benchmark

Vinaka Vinaka
 Dankscheen Dankscheen
 Maake Maake
 Kiiitos Kiiitos
 Asante Asante
 Dhanyavadagalu Dhanyavadagalu
 Shukria Shukria
 Manana Manana
 Dankon Dankon
 Kam Sah Hammida Kam Sah Hammida
 اراكش اراكش
 Mauruuru Mauruuru
 Biyan Biyan
 Diolch i Chi Diolch i Chi
 Terima Kasih Terima Kasih
 Matondo Matondo
 Dank Je Dank Je
 Chokrane Chokrane
 Tack Tack
 Blagodaram Blagodaram
 Ngiyabonga Ngiyabonga
 Dziekuje Dziekuje
 Arigato Arigato
 Grazi Grazi
 Juspaxar Juspaxar
 Gracias Gracias
 Mochchakkeram Mochchakkeram
 Tack Tack
 Grazi Grazi
 Bedankt Bedankt
 Dakujem Dakujem
 Tingki Tingki
 धन्यवाद धन्यवाद
 cảm ơn bạn cảm ơn bạn
 Ua Tsaug Rau Koj Ua Tsaug Rau Koj
 Bedankt Bedankt
 Dakujem Dakujem
 Grazas Grazas
 Gratias Tibi Gratias Tibi
 Obrigado Obrigado
 Dėkuji Dėkuji
 Nirringrazzjak Nirringrazzjak
 Kia Ora Kia Ora
 Kop Khun Kop Khun
 Paldies Paldies
 Suksama Suksama
 Nuwun Nuwun
 ありがとう ありがとう
 Misaotra Misaotra
 Rahmat Rahmat
 Matur Matur
 Nuwun Nuwun
 谢谢 谢谢
 XBalala XBalala
 Welalin Welalin
 Di Ou Mési Di Ou Mési
 Merci Merci
 Go Go
 Raibh Maith Raibh Maith
 Agat Agat
 Djere Dieuf Djere Dieuf
 Eskerrik Ask Eskerrik Ask
 Najis Tuke Najis Tuke