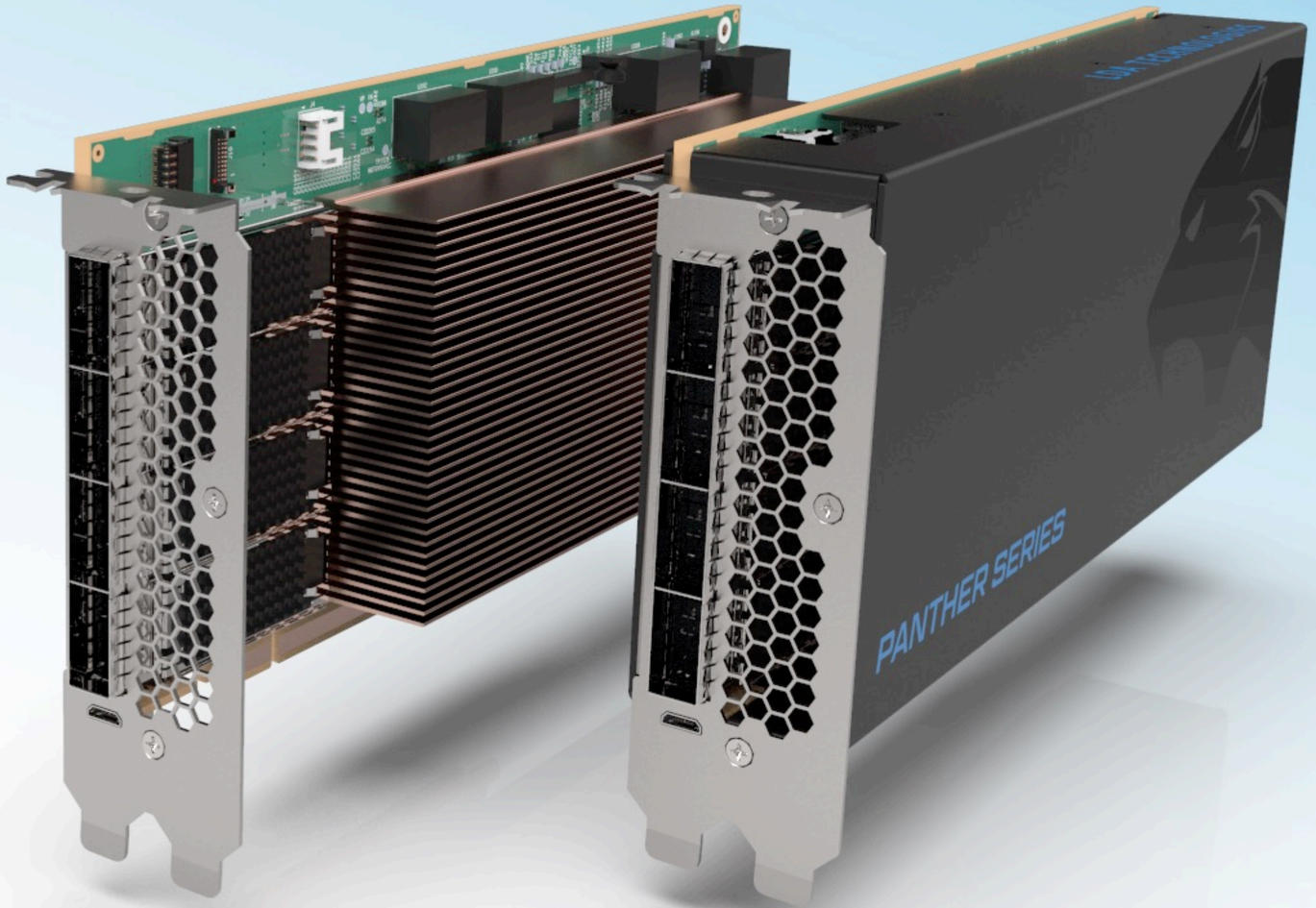
LATENCY-OPTIMIZED FPGA ACCELERATION PLATFORMS

LDA TECHNOLOGIES™

Latency-Optimized FPGA Acceleration Platforms

LDA SBM Board

- Xilinx VU9P -3 FPGA
- 576 MB static memory with 12 ns read latency (9 ns write)*
- 2 Jitter attenuators to synchronize with two incoming data flows



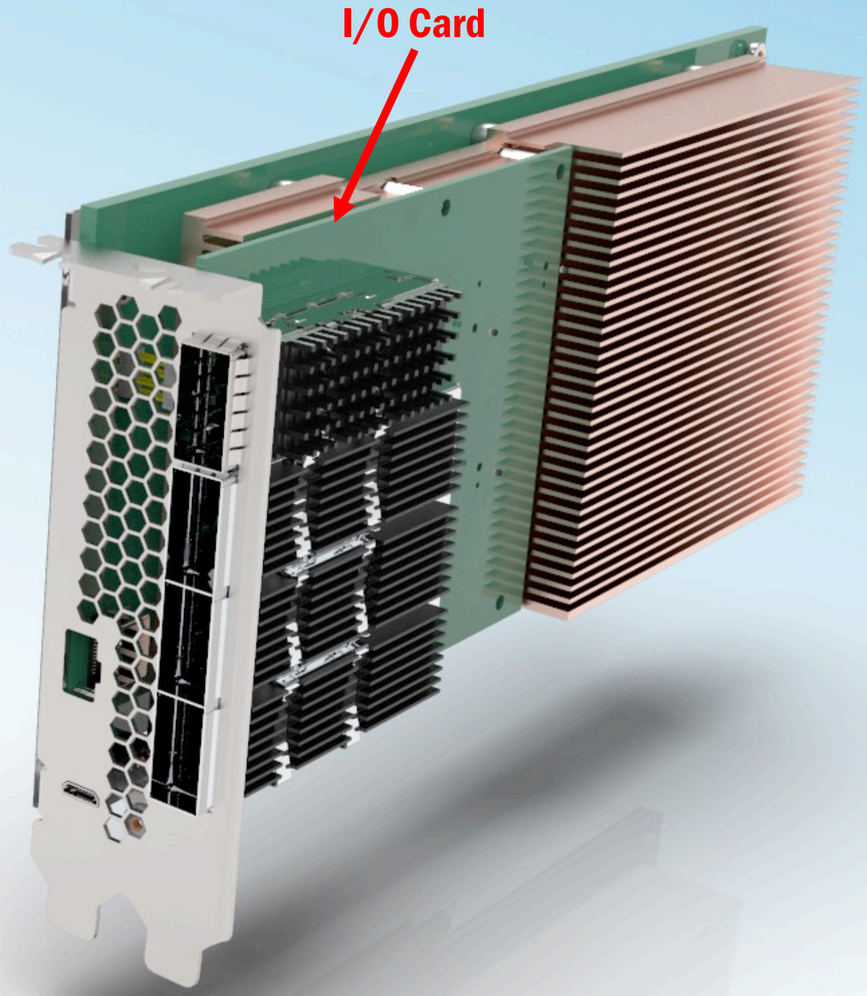
*Not STAC Benchmarks

Latency-Optimized FPGA Acceleration Platforms

LDA Versal Board

- Xilinx VP1202 / VP1502 FPGA
- 432 MB static memory with 10.5 ns read latency*
- 2 Jitter attenuators to synchronize with two incoming data flows
- PCIe 3.0/4.0/5.0 with multi-hosting support
- Various I/O card options:
  - Multiple cards with different ports: QSFP, SFP, QSFPDD
  - L1 Crosspoint
  - Open for customizations

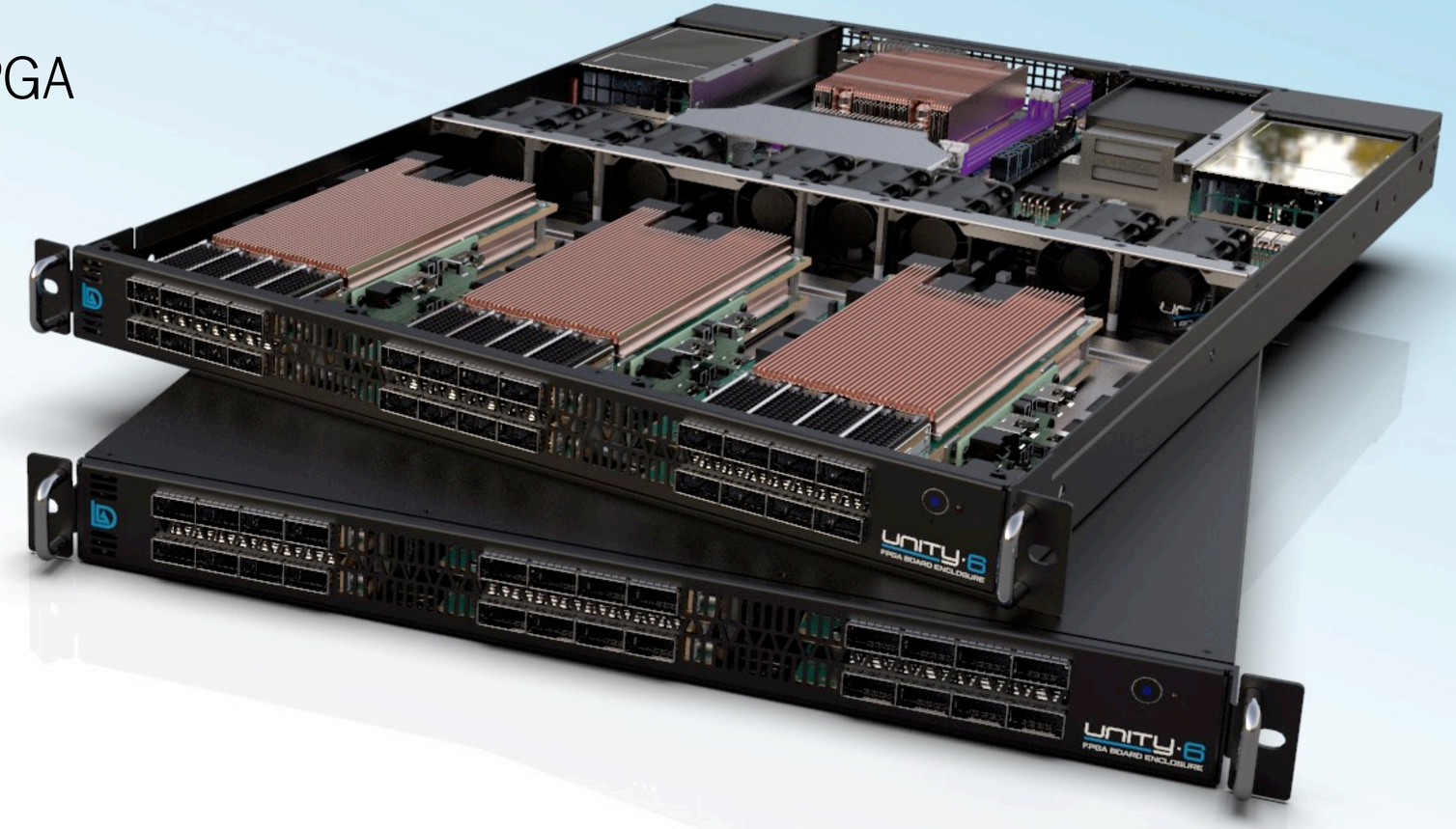
*Not STAC Benchmarks



Latency-Optimized FPGA Acceleration Platforms

FPGA Boards Server

- Up to 6 LDA FPGA boards or Xilinx Alveo boards
- 64-core AMD EPYC Rome/Milan CPU
- 64 PCIe 4.0 lanes shared among FPGA boards
- Industry standard server management tools



Thank You!

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