



# Xilinx Accelerated Algorithmic Trading

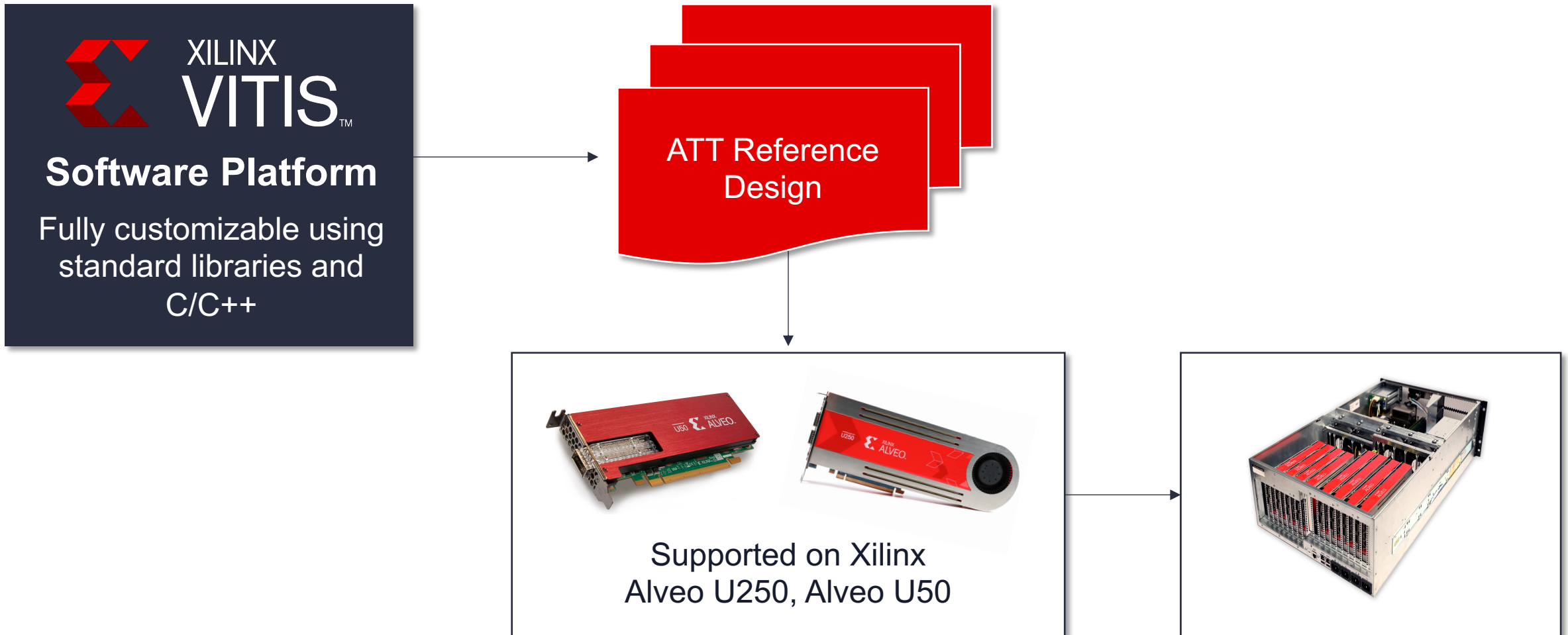
FPGA Trading Made Easy

May 2021



# Xilinx Accelerated Algorithmic Trading (AAT)

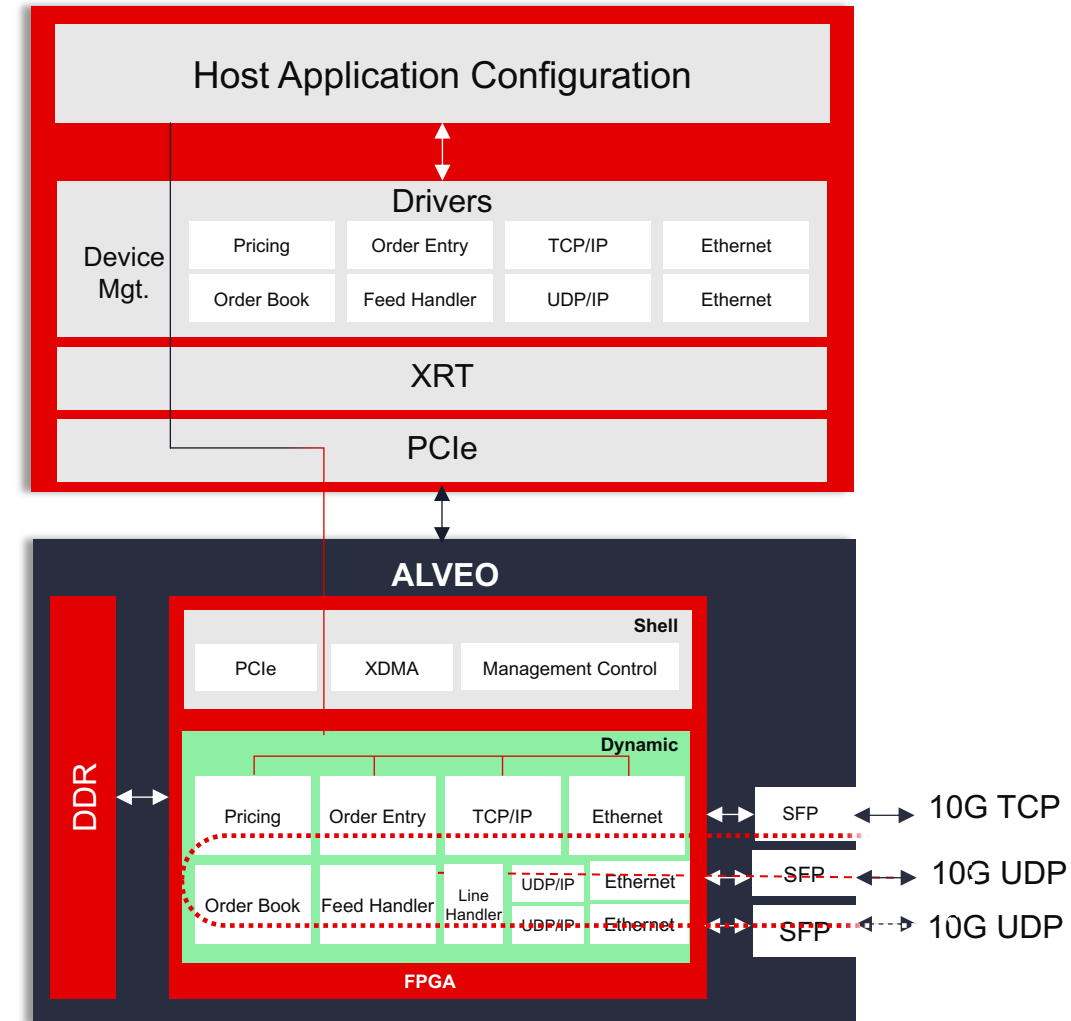
Customizable, open-source trading reference design



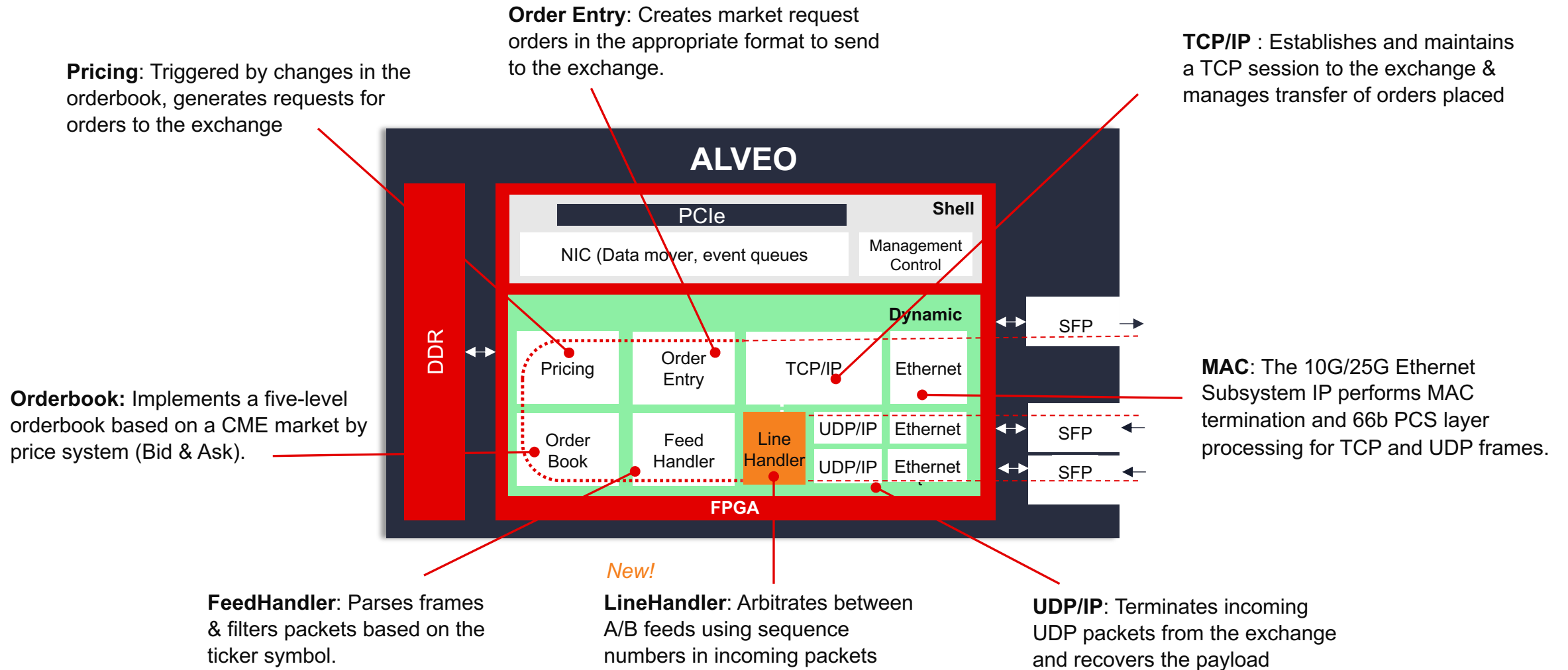
# AAT accelerates migration from CPU to FPGA

All the building blocks to **enable software development of an electronic trading system** on Xilinx Alveo products

- ✓ Fully written in HLS
- ✓ Designed for Software Engineers
- ✓ Integrated with Vitis
- ✓ Runs out of the box on Xilinx Alveo
- ✓ Source code provided



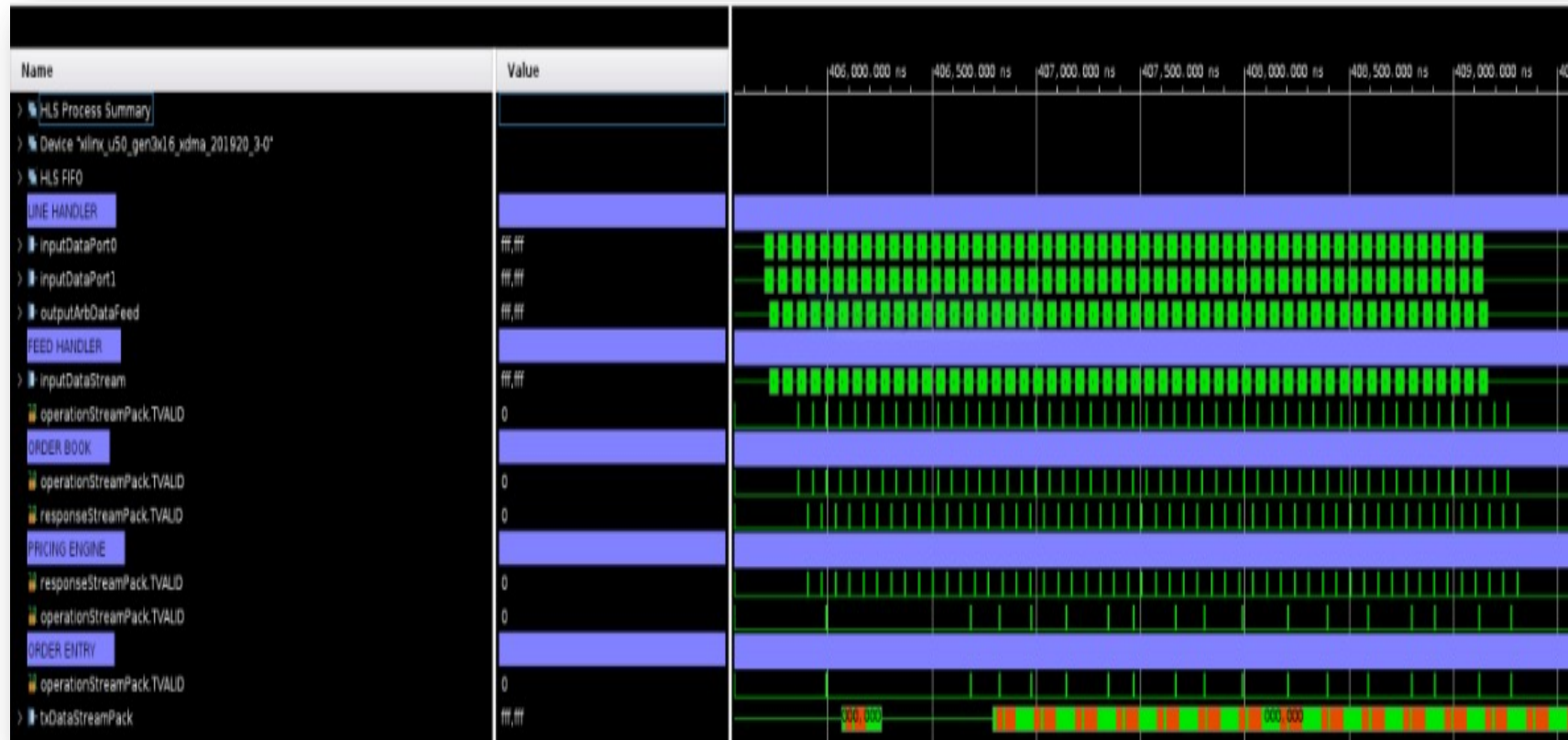
# ATT Modular Design Explained



Each module can be used as is, or customized to create a tailored trading solution

# Introducing Hardware Emulation!

**Cycle Accurate Simulation For The Complete Design**

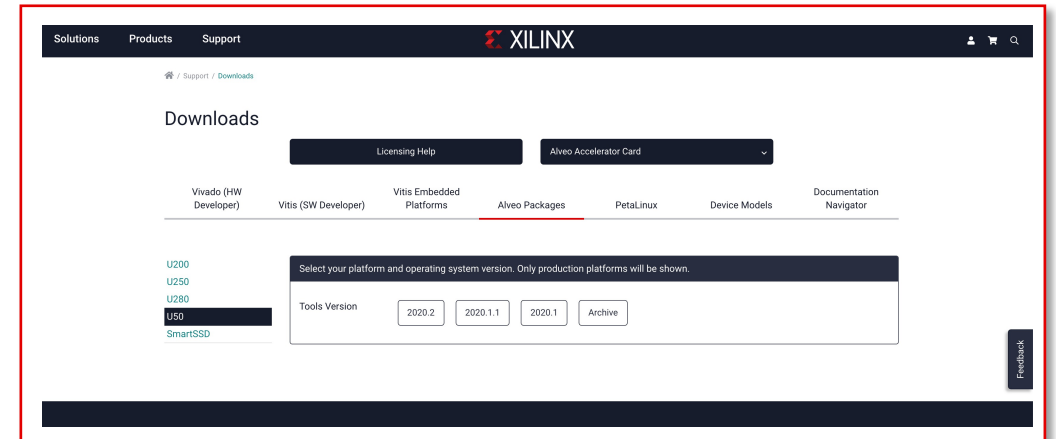


# Get Started With Xilinx Accelerated Algorithmic Trading

Regain the latency edge



Alveo U50 & U250 - available  
through Xilinx VARs and VADs



Free open-source download from  
Xilinx.com, no license fees

[www.xilinx.com/applications/data-center/financial-technology/accelerated-algorithmic-trading.html](http://www.xilinx.com/applications/data-center/financial-technology/accelerated-algorithmic-trading.html)



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# Thank You

