EEEXABLAZE How hard could it be?

Understanding network traffic at the picosecond level



- Low(er) latency PCS/MAC



- Low(er) latency PCS/MAC
- PCIe DMA Engine



- Low(er) latency PCS/MAC
- PCIe DMA Engine
- HW Accelerated TCP Engine (ATE)



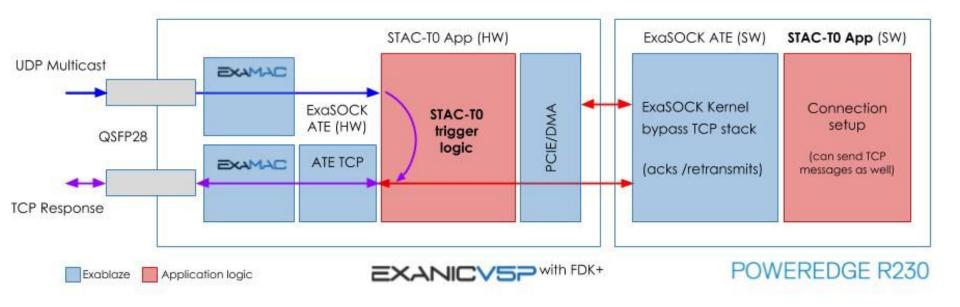
- Low(er) latency PCS/MAC
- PCIe DMA Engine
- HW Accelerated TCP Engine (ATE)
- Software, drivers, tooling



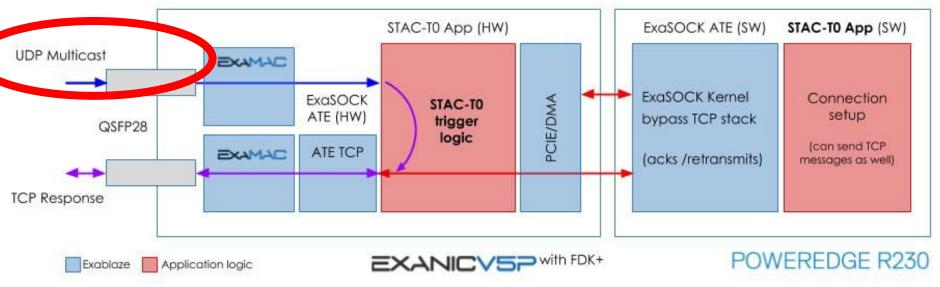
- Low(er) latency PCS/MAC
- PCIe DMA Engine
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- Software, drivers, tooling
- Extensive examples.

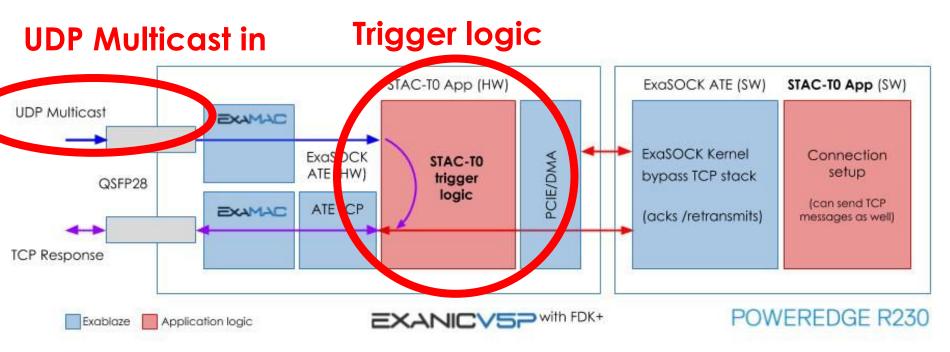


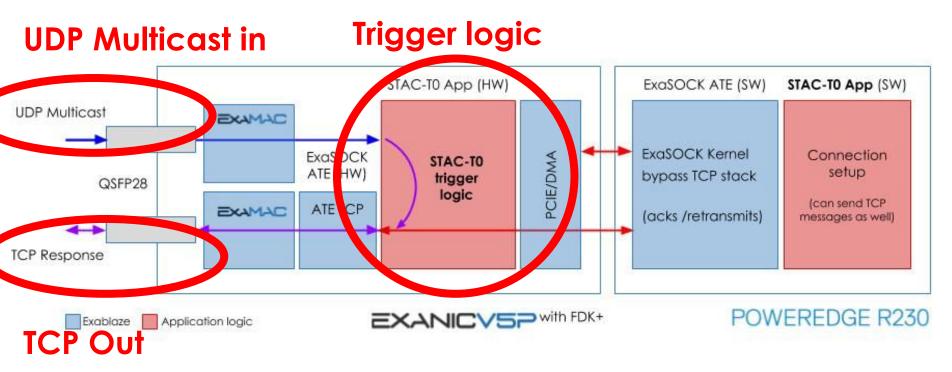
- Low(er) latency PCS/MAC
- PCIe DMA Engine
- HW Accelerated TCP Engine (ATE)
- Software, drivers, tooling
- Extensive examples.



UDP Multicast in







31 ns* Min. actionable latency

* Subject to final validation

STAC-T0.β1.*.*.ACTIONABLE.MIN)

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Min. actionable latency

* Subject to final validation

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31 ns*

Min. actionable latency

* Subject to	final	validation	

STAC-T0.β1.*.*.ACTIONABLE.MIN)

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1. STAC can't measure things

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2. It's harder than it looks

1. STAC can't measure things

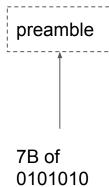
2. It's harder than it looks

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Enter the Picosecond

Problem:

When did a field in my packet arrive?



An Ethernet fame:

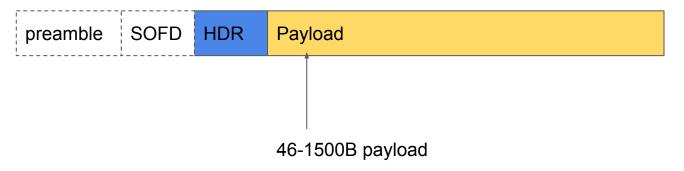
preamble SOFD

Start of frame delimiter (1B)

An Ethernet fame:



14B - SRC/DST MAC address, ether type



An Ethernet fame:

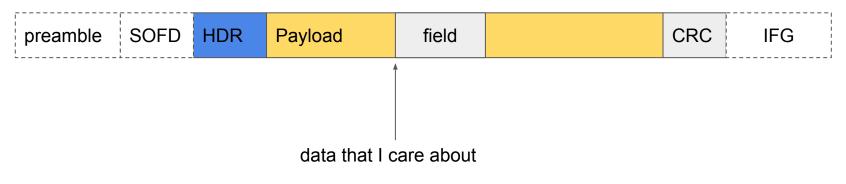


4B (32b) frame check sequence

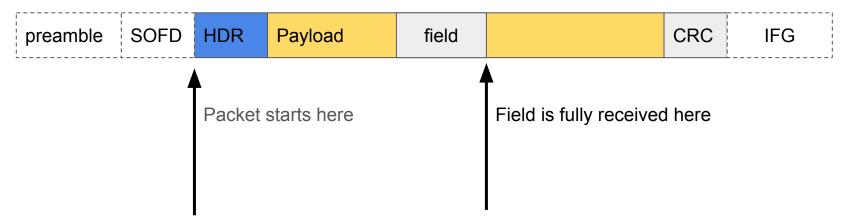
An Ethernet fame:

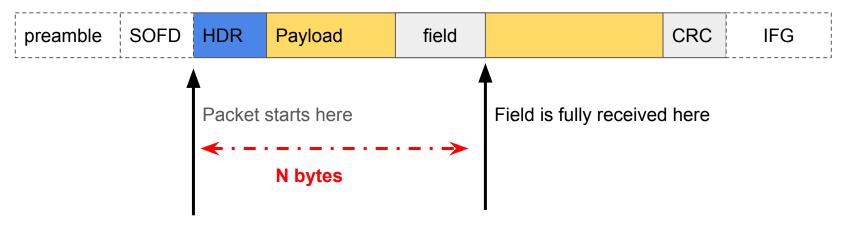
preamble	SOFD	HDR	Payload	CRC	IFG

96b interframe gap









Bytes offset into the packet



Convert to bits Delay = N x 8

Line rate (G-bits per second) Delay = N x 8 x 1/ 10 Gb/s

Convert to picoseconds (10⁻¹²) Delay = N x 8 x 1/ 10 Gb/s x 1/ps

Ideal calculation Cancels out Delay = N x 8 x 100

simplifies Delay = N x 800

42B

Delay = 42B

Delay = 42 x 800 =

Delay = 42 x 800 = 33,600ps

Finished?

Line rate (G-bits per second) Delay = N x 8 x 1/ 10 Gb/s

Line rate (G-bits per second) Delay = N x 8 x (1/ 10 Gb/s

Does 10G Ethernet really run at 10 Gb/s?

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Line rate (G-bits per second) Delay = N x 8 x (1/ 10 Gb/s)

Does 10G Ethernet really run at 10 Gb/s? No!

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- Uses 64b/66b encoding

- Uses 64b/66b encoding

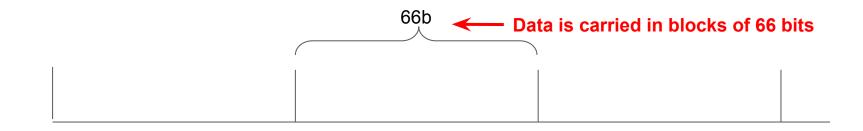
- 66/64 x 10 = 10.3125Gb/s *bit rate*

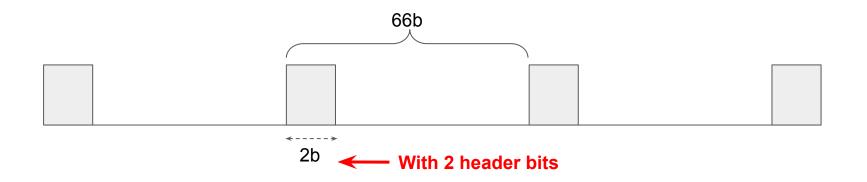
- Uses 64b/66b encoding

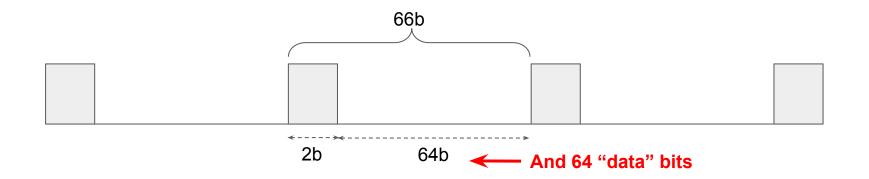
- 66/64 x 10 = 10.3125Gb/s bit rate

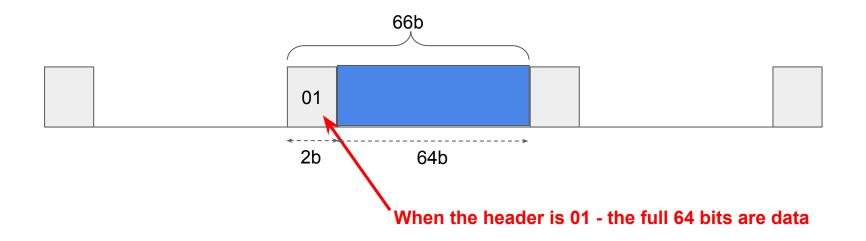
*not a STAC measurement

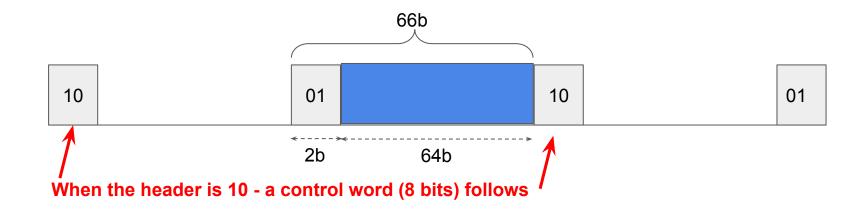
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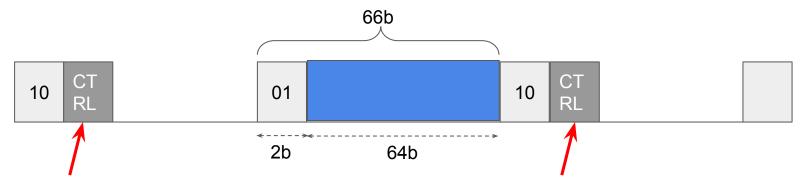




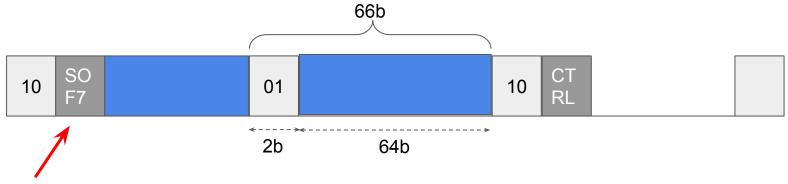




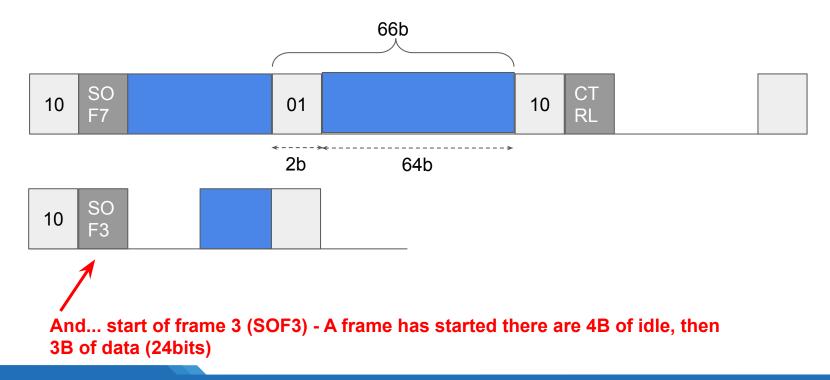


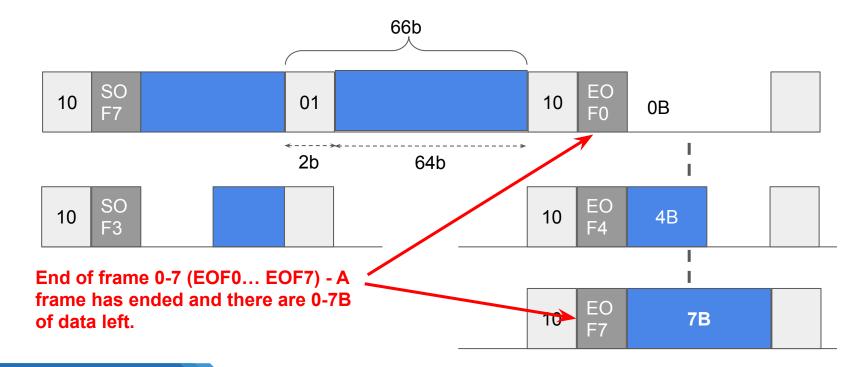


The control word can have a number of values (256), but the most important ones for this discussion are ...



Start of frame 7 (SOF7) - A frame has started there are 7B (56b) of data



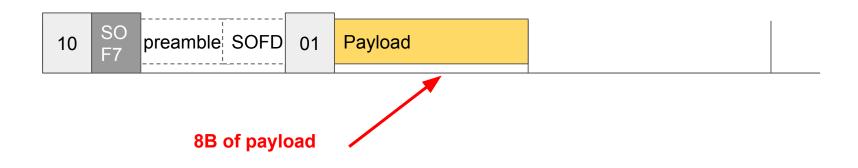


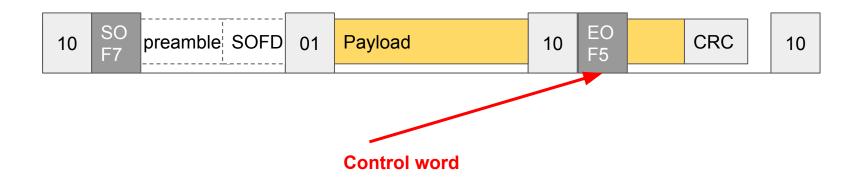


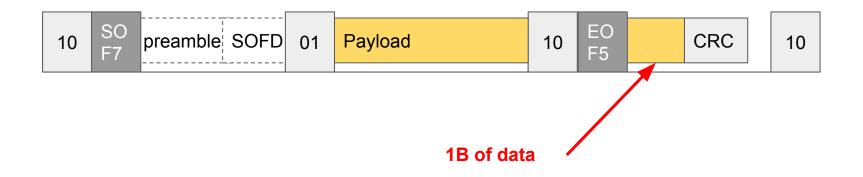


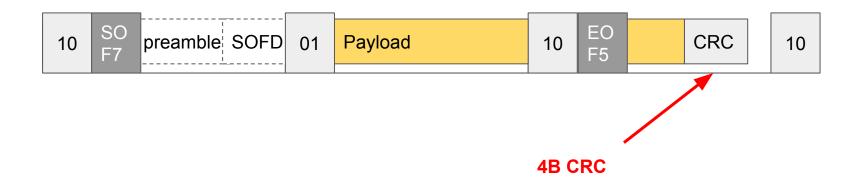


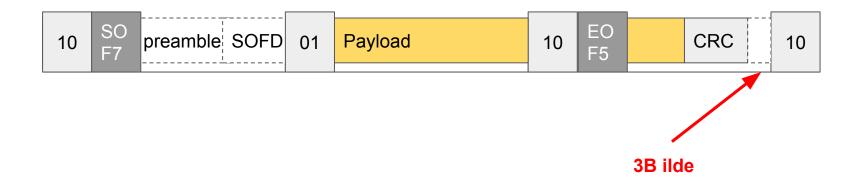












10 SO preamble SOFD 01	Payload	10 EO F5	CRC 10
------------------------	---------	-------------	--------

Possible reasons

1. STAC can't measure things



Where's the hard part??

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Considering the encoding layer raises questions....

- When does the frame start? And when it it timestamped?

- When does the frame start? And when it it timestamped?
- When does the frame end?

- When does the frame start? And when it it timestamped?
- When does the frame end?
- How long is the frame? (in bits and in picoseconds)

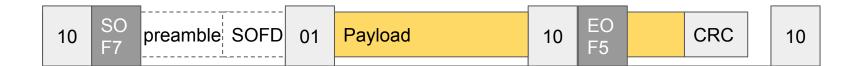
- When does the frame start? And when it it timestamped?
- When does the frame end?
- How long is the frame? (in bits and in picoseconds)
- How far (ps) into the frame is an arbitrary offset?

Encoding layer questions....

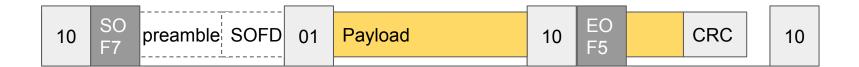
- When does a frame start? (and is timestamped?)

10	SO F7 preamble SOFD	01	Payload	10	EO F5	CRC	10

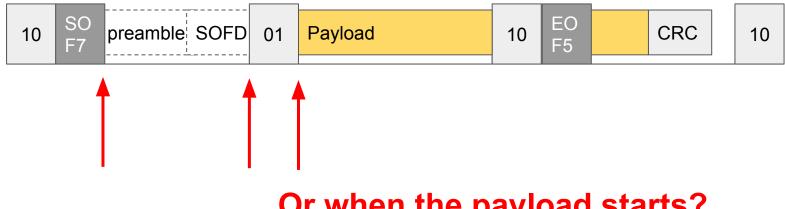
When the 64/66 SOF7 signal is found?



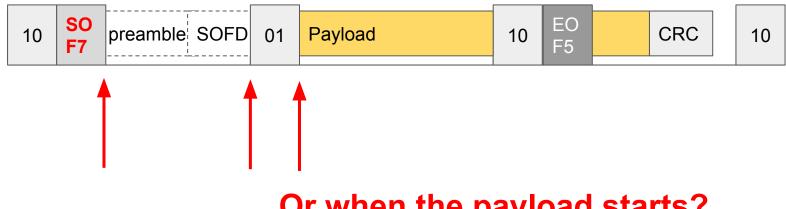
Or when the SOFD finishes?



Or when the payload starts?

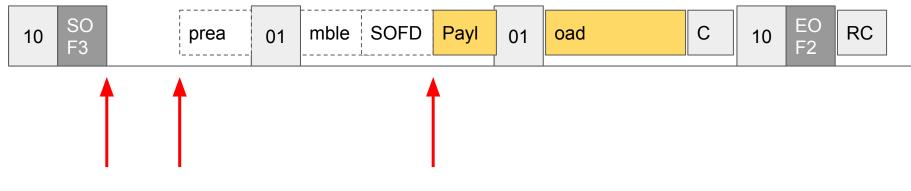


Or when the payload starts?



Or when the payload starts?

When does a frame start? And when is it timestamped

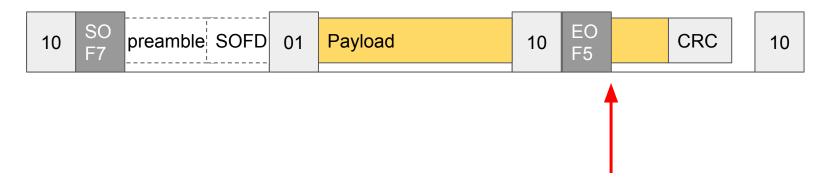


What about the SOF3 case?

Encoding layer questions....

- When does the frame start? And when is it timestamped?
- When does the frame end?

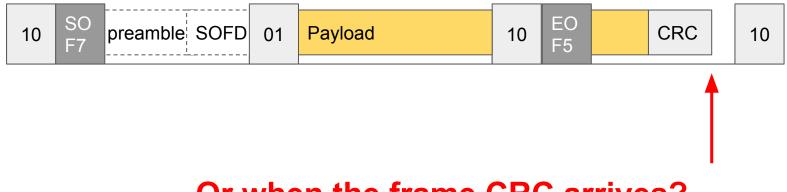
When does a frame end?



When the 64/66 EOF1-7 signal is found?

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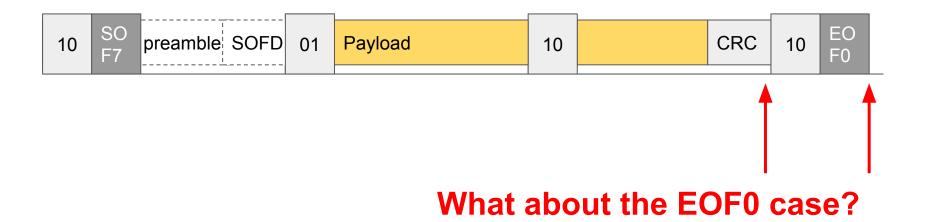
When does a frame end?



Or when the frame CRC arrives?

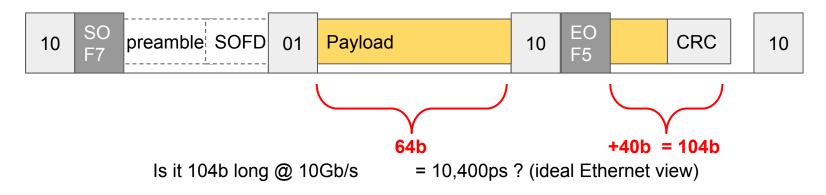
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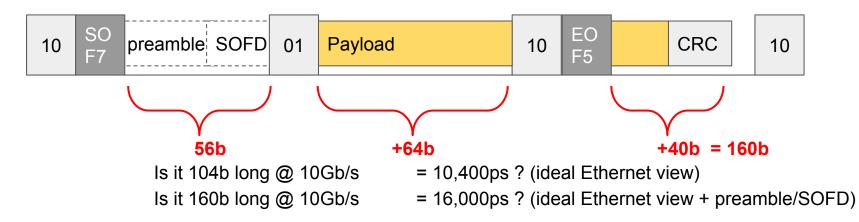
When does a frame end?

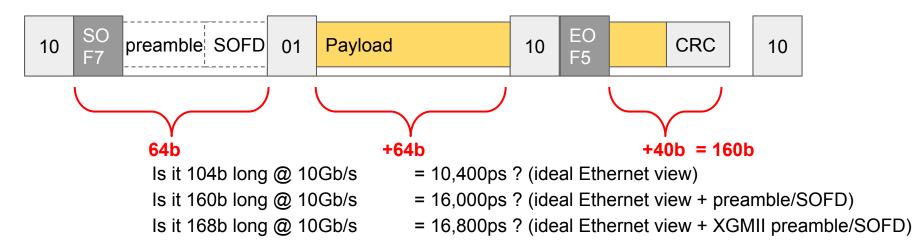


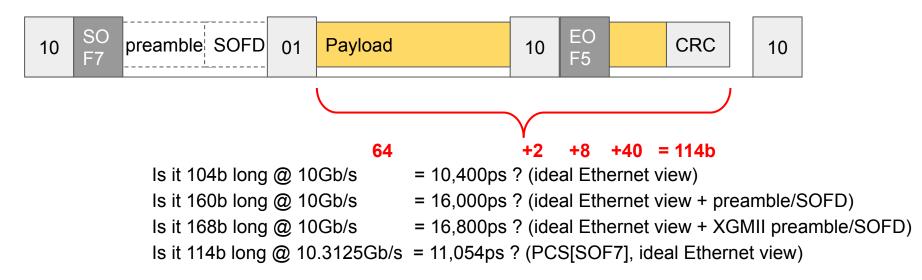
Encoding layer questions....

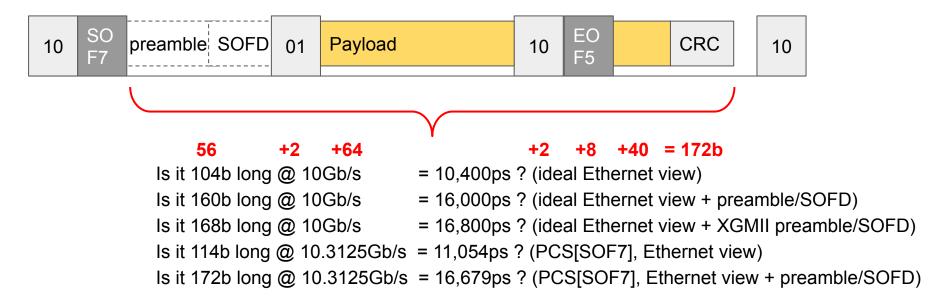
- When does the frame start? And when is it timestamped?
- When does the frame end?
- How long is a frame?

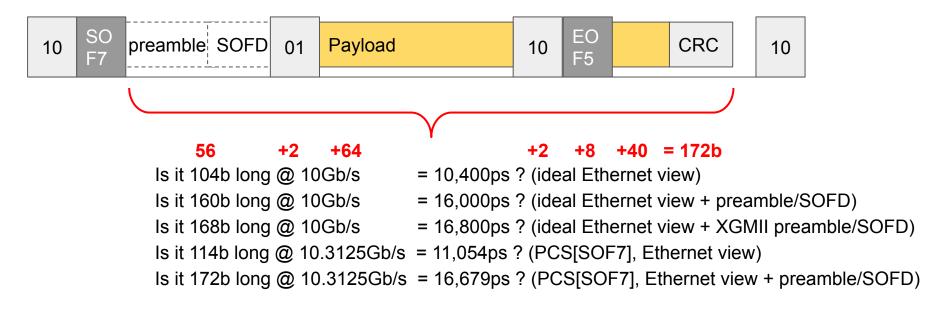




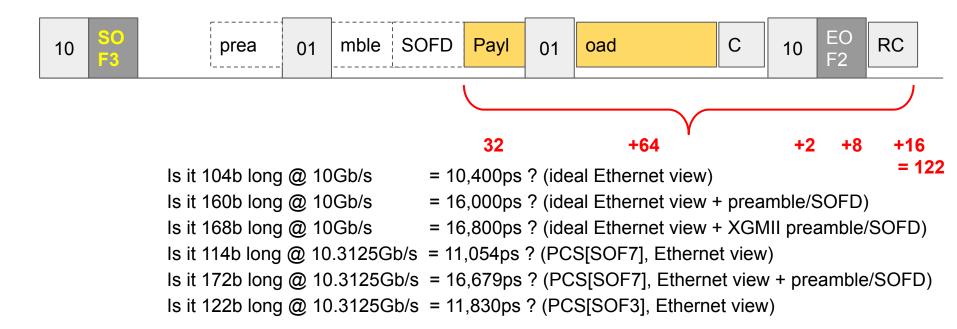


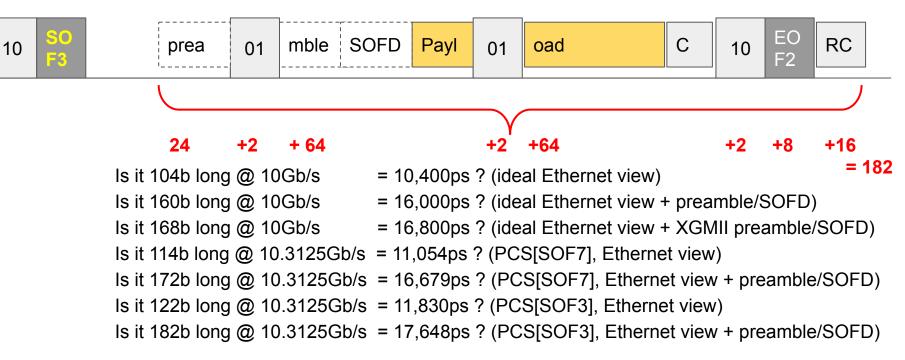


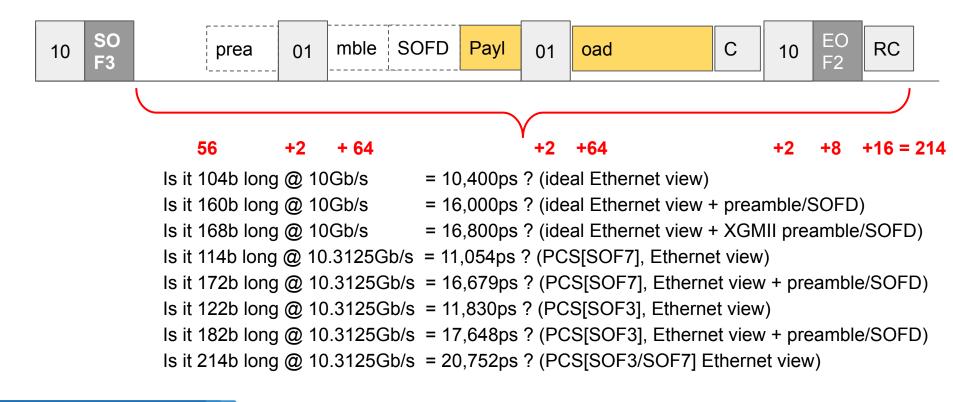


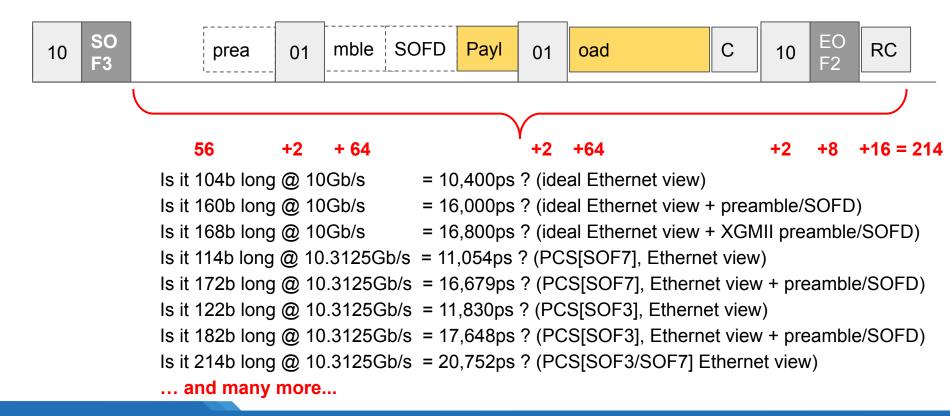


What about the SOF3 case?



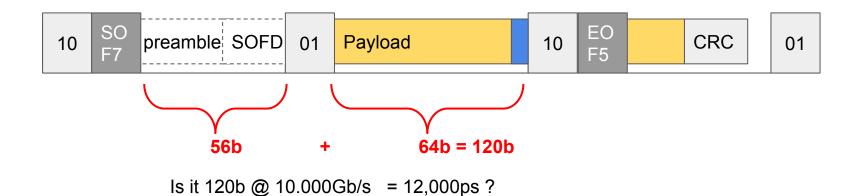


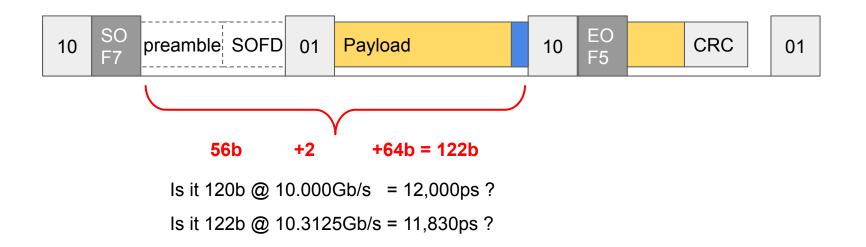


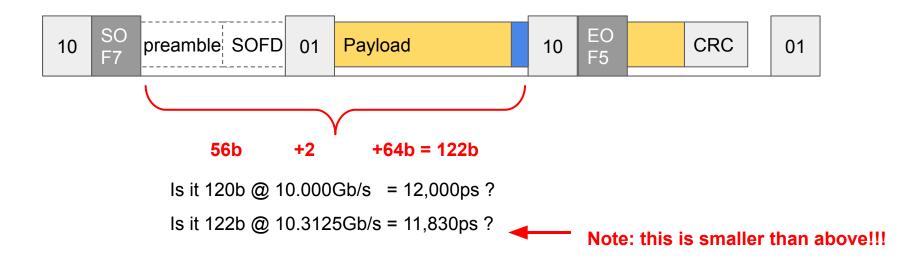


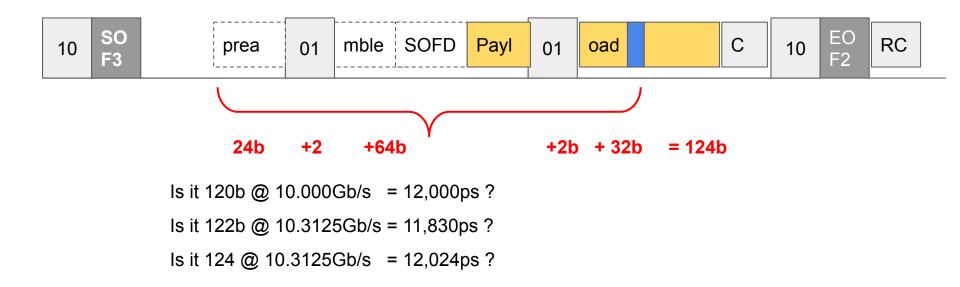
Encoding layer questions....

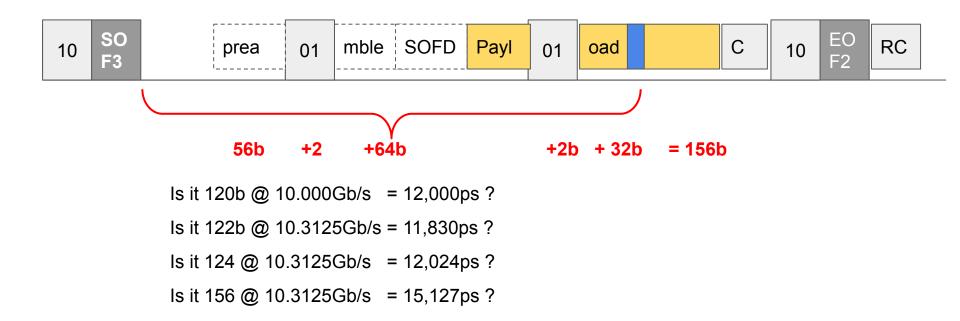
- When does the frame start? And when it it timestamped?
- When does the frame end?
- How long is the frame? (in bits and in picoseconds)
- How far (ps) into the frame is an arbitrary offset?

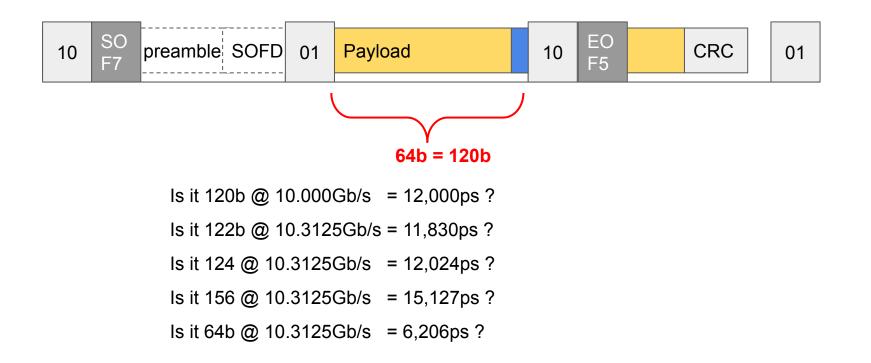


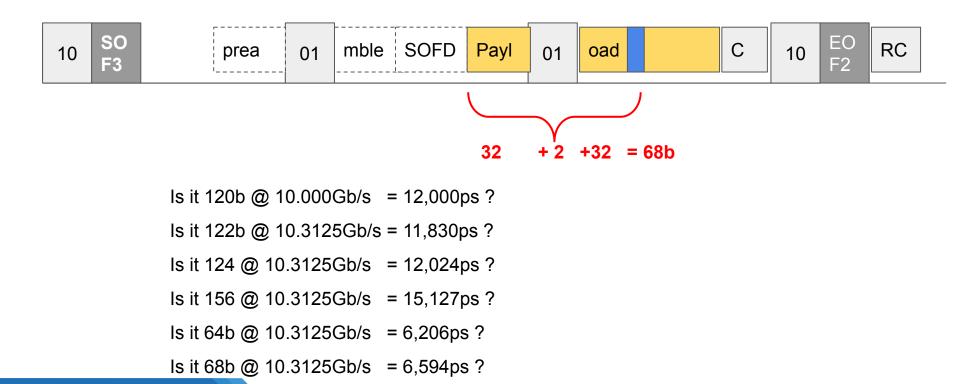












Implications for uncertainty

 Ethernet protocol has an average rate of 10Gb/s at layer 2, but PCS effects are visible at individual packet sizes.

Implications for uncertainty

- 1. Ethernet protocol has an **average** rate of 10Gb/s at layer 2, but PCS effects are visible at individual packet sizes.
- 2. At picosecond scales, PCS encodings must be taken into account and 10.3125Ghz must be used.

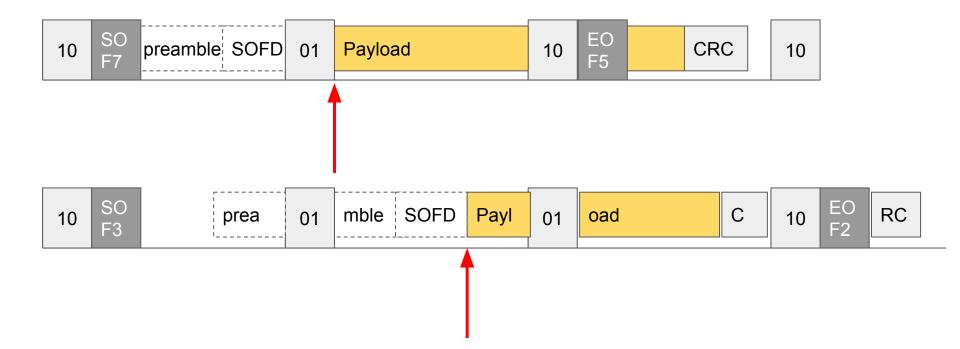
Implications for uncertainty

- 1. Ethernet protocol has an **average** rate of 10Gb/s at layer 2, but PCS effects are visible at individual packet sizes.
- 2. At picosecond scales, PCS encodings must be taken into account and 10.3125Ghz must be used.
- 3. Timestamps at PCS SOF3/7 and Ethernet layer SOFD have different absolute offsets*. Since both SOF3 and SOF7 may appear, these need to be accounted for.

- When does the frame start?

- When does the frame start? At the start of the payload (DST MAC)

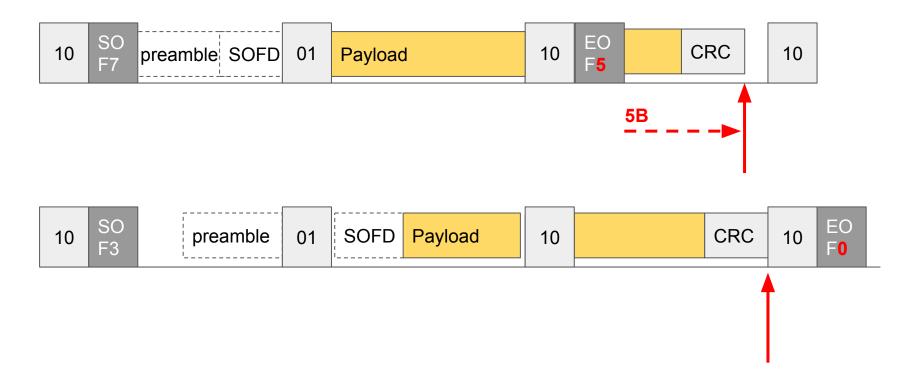
When does a frame start? At the start of the payload (DST MAC)



- When does the frame start? At the start of the payload
- When does the frame end?

- When does the frame start? At the start of the payload
- When does the frame end? At the end of the CRC

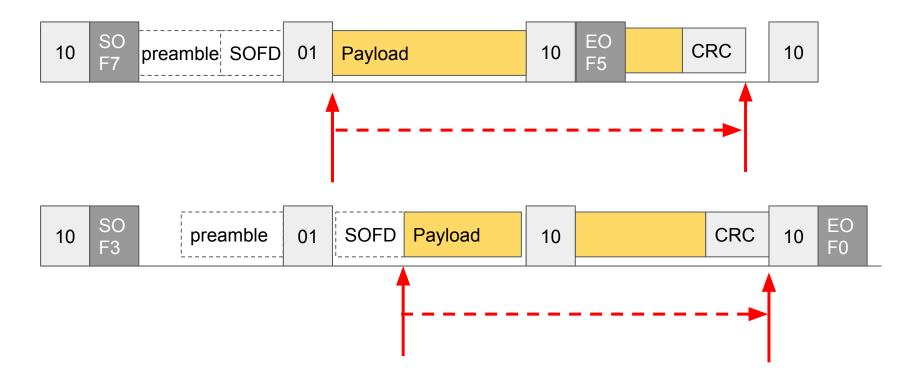
When does a frame end? At the end of the CRC



- When does the frame start? At the start of the payload
- When does the frame end? At the end of the CRC
- How long is the frame?

- When does the frame start? At the start of the payload
- When does the frame end? At the end of the CRC
- How long is the frame? (CRC payload) @ 10.3125G

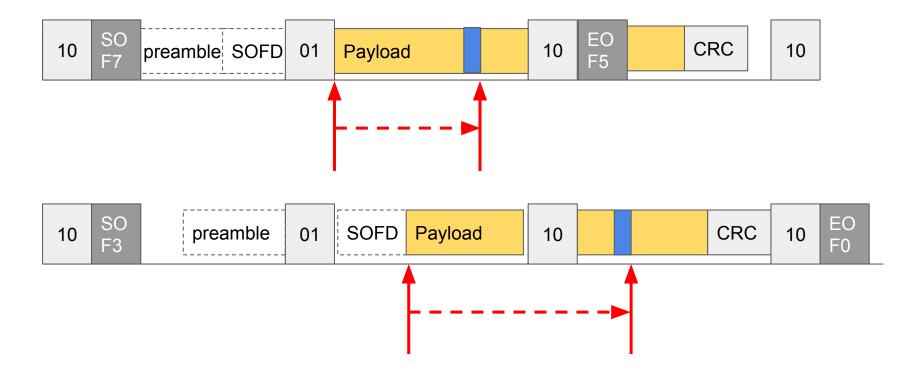
How long is the frame? (CRC - payload) @ 10.3125Gbs



- When does the frame start? At the start of the payload
- When does the frame end? At the end of the CRC
- How long is the frame? (CRC payload) @ 10.3125G
- How far (ps) is an offset?

- When does the frame start? At the start of the payload
- When does the frame end? At the end of the CRC
- How long is the frame? (CRC payload) @ 10.3125G
- How far (ps) is an offset? (bit offset payload) @ 10.3125G

How far is an offset?? (bit offset - payload) @ 10.3125G



Our recommendations!

- When does the frame start? At the start of the payload
- When does the frame end? At the end of the CRC
- How long is the frame? (CRC payload) @ 10.3125G
- How far (ps) is an offset? (bit offset payload) @ 10.3125G

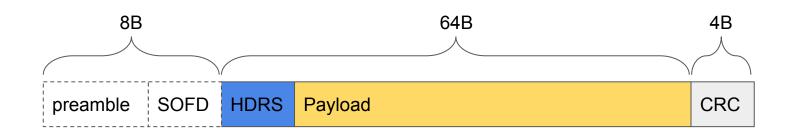
Worked example

1. Message is 64B long, excluding FCS (4B)

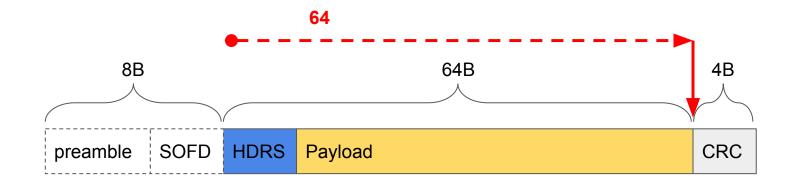
Worked example

- 1. Message is 64B long, excluding FCS (4B)
- 2. How long is the frame? (in ps)

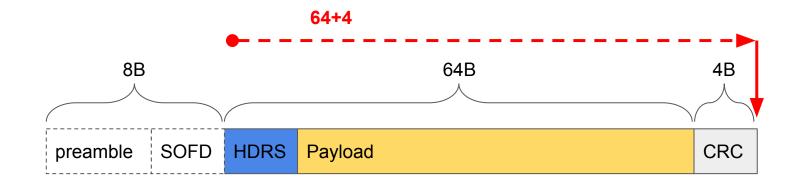
Ideal view of 64B Fame



Ideal view of 64B Fame



Ideal view of 507B Fame



$Delay = N \times 800$

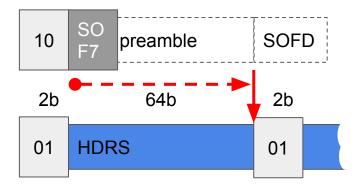
Delay = 68 x 800

Delay = 68 x 800 = 54,400ps

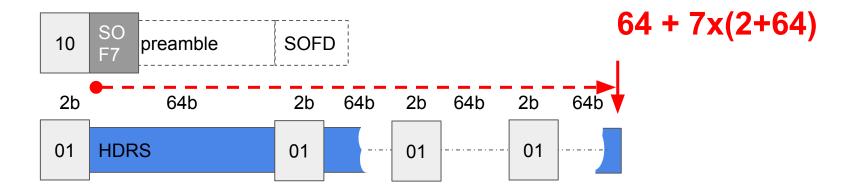
Delay = 68 x 800 = 54,400ps

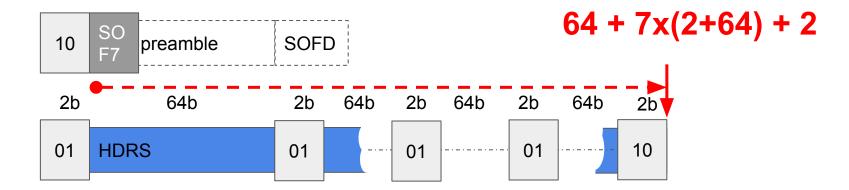


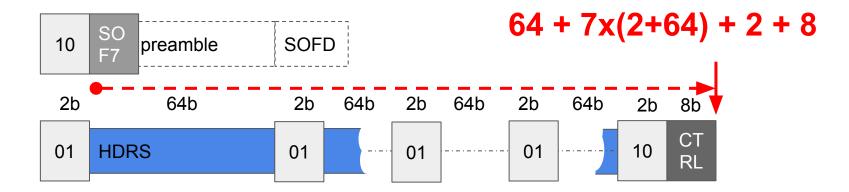
PCS SOF7 view of a 68B fame:

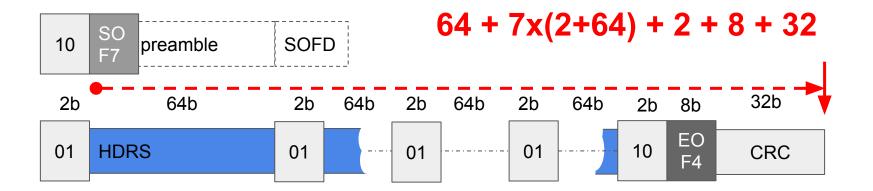


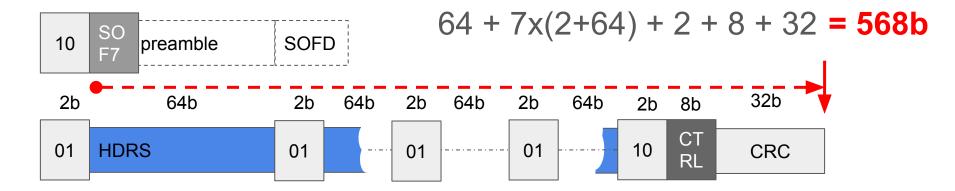
64b













, bits in the packet

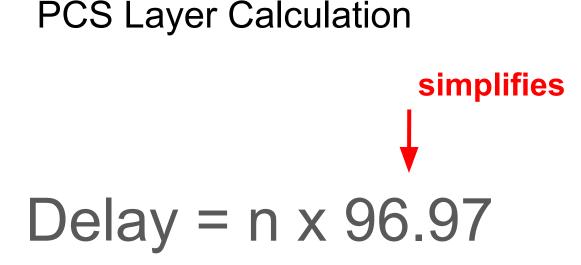


PCS Layer Calculation

picoseconds Delay = n x 1000

bit-rate Delay = n x 1000 / 10.3125

PCS Layer Calculation



PCS Layer Calculation

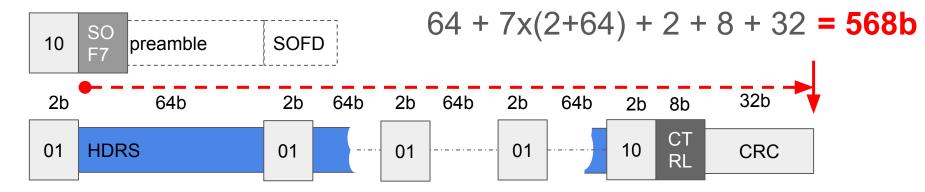
Delay = **568** x 96.97

PCS Layer Calculation

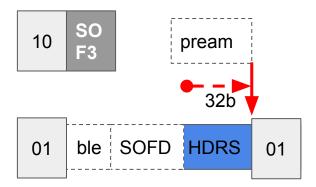
Delay = 568 x 96.97 = **55,079ps**

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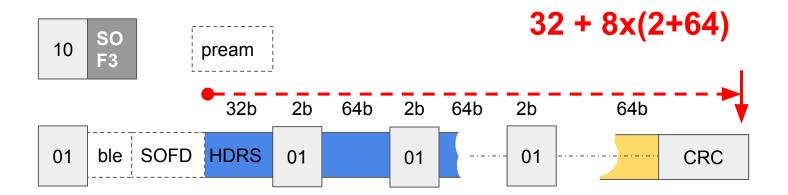
Taking into account PCS layer effects... PCS SOF7 view of a 68B fame:

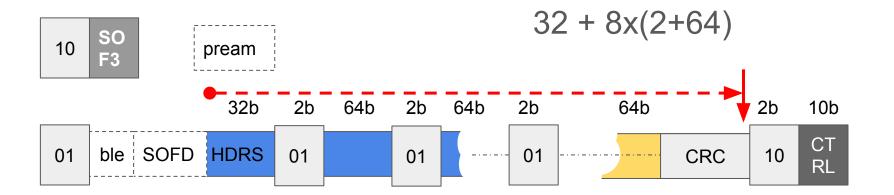


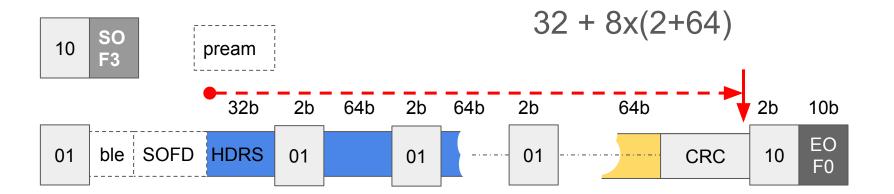
PCS SOF3 view of a 68B fame:



32

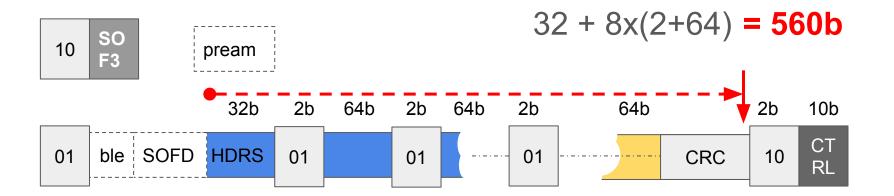






Our recommendations!

- When does the frame start? At the start of the payload
- When does the frame end? At the end of the CRC
- How long is the frame? (CRC payload) @ 10.3125G
- How far (ps) is an offset? (bit offset payload) @ 10.3125G



PCS Layer Calculation

Delay = 560 x 96.97 = **54,303ps**

Ideal frame length: 55,400ps

Ideal frame length: 55,400ps

SOF7 fame length: 55,079ps

Ideal frame length: 54,400ps

SOF7 fame length: 55,079ps

SOF3 frame length: 54,303ps

Ideal frame length: 54,400ps

SOF7 fame length: 55,079ps

SOF7 is 679ps longer than ideal

SOF3 frame length: 54,303ps

Ideal frame length: 55,400ps

SOF7 fame length: 55,079ps

SOF3 frame length: 54,303ps

SOF3 is 97ps shorter than ideal

Ideal frame length: 54,400ps

SOF7 fame length: 55,079ps

SOF3 frame length: 54,303ps

SOF3 is 776ps shorter than SOF7

Conclusions

1. It's harder than it looks to do measurements at the picosecond scale.

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Conclusions

- 1. It's harder than it looks to do measurements at the picosecond scale.
- 2. Vendors need to specify where/when timestamps are taken to facilitate index offset/frame length calculations
- 3. When taking into account PCS layer effects in the STAC-T0 benchmark one frame is 776ps longer/later than expected.

Questions?

(or tick the box)

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