

4th Generation Platform Scale – Latency - Versatility

STAC - Fall Summits 2018 Chicago, New York, London



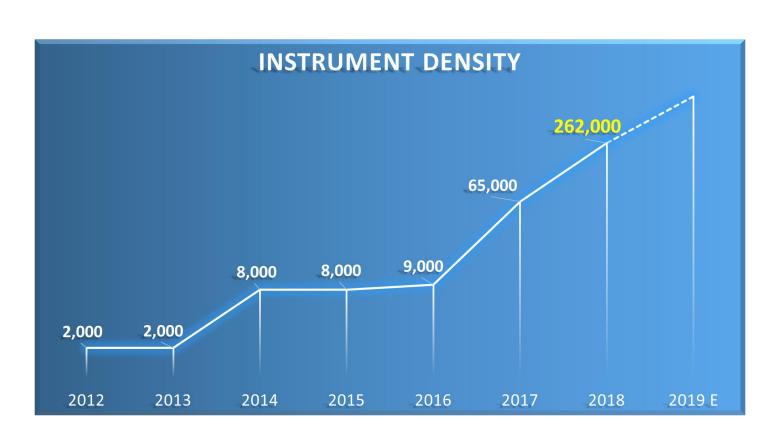
SCALE

Biggest UltraScale+ FPGA

Biggest QDR Memories

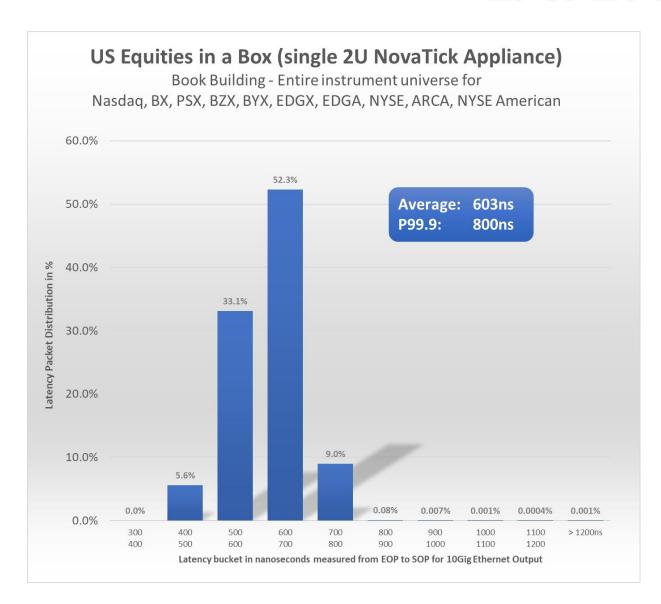
Custom hardware design

Massive I/Os





LATENCY



US Equity: 20% improvement

603ns average 800ns at p99.9

CME:30% improvement

450ns average 800ns at p99.9



VERSATILITY



2 hardware form factors



Ideal for:

- Enterprise deployments
- Full US Equity deployments
- Large number of trading servers

Ideal for:

- Cost efficient low TCO deployments
- Small number of trading servers
- Tick to trade implementations



AND STILL...

- 60 feed global coverage and counting
- L1 book, L2 book and L3 book consumption options
- All the expected robustness features:
 Recovery, retransmission, snapshot, A/B arbitration
- NovaSparks Wire Format for the tightest integration
- NovaSparks API
 for the quickest integration
- PTP Time stamping
- Microwave Shaped and Conflated Output
- NovaLink and Tick to Trade Development platform with Solarflare

THANK YOU...

