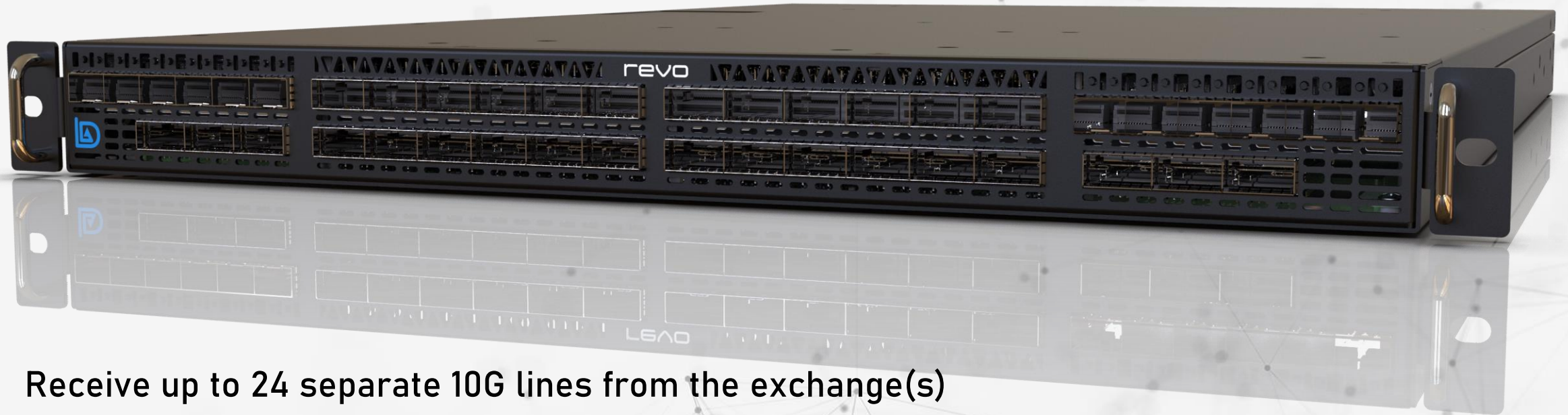


LDA TECHNOLOGIES™

## Introducing LDA Revo

A specialized high-density device for line arbitration

# Use Case



- Receive up to 24 separate 10G lines from the exchange(s)
- Deliver the data from each line to 12 FPGA boards
- Enable each FPGA board to send data to any line
- **At least 312 (!) 10G links:  $24 + 24 \times 12 = 312$**

# LDA Revo

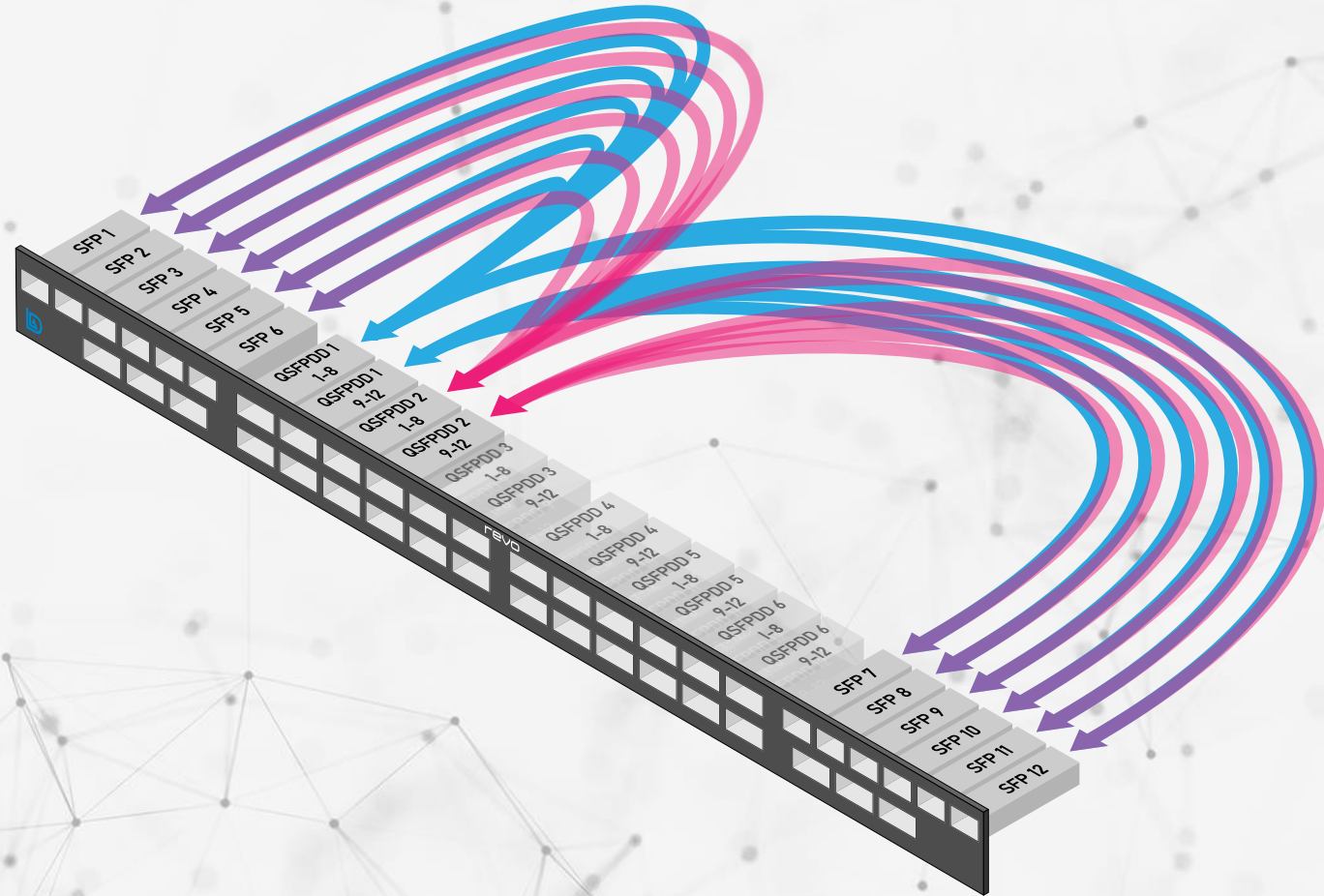
- 12x SFP+ ports for exchange connectivity
- 24x QSFPDD ports (12 port pairs) for downstream (FPGA board) connectivity
- 6x QSFPDD ports for housekeeping/management tasks or external timestamping/capture





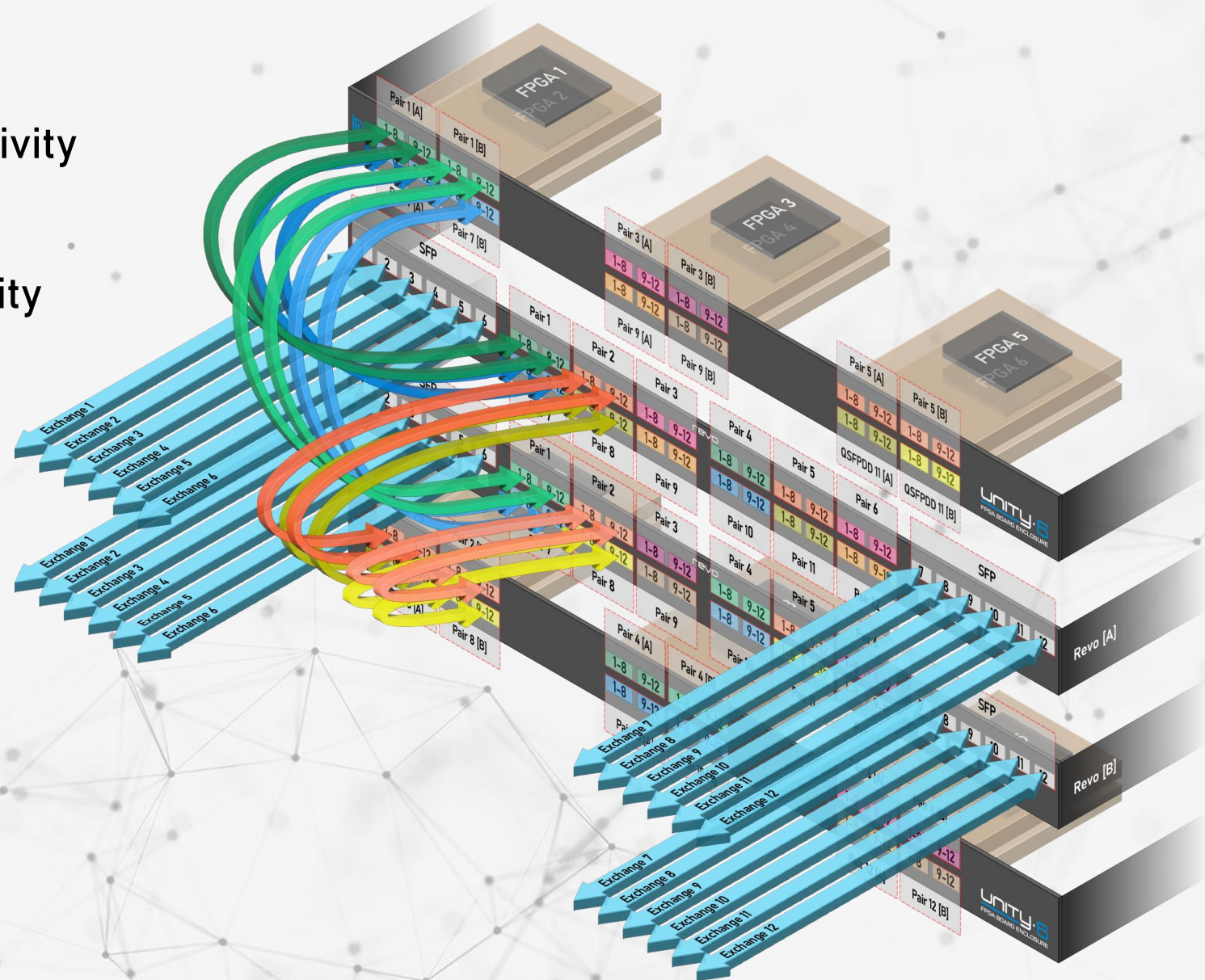
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- Configurable Layer 1 for SFP+ output
- Downstream interconnect with 2"-7" QSFPDD copper cables
- Internal tap aggregation and timestamping option with the complete LDA NeoTap features for capture and monitoring purposes.





Thank You

Maake Asante Shukria Շնորհակալություն Manana Dankon արևոյն

Vinaka kōszönöm Kiitos Kam Sah Hammida արևոյն

감사합니다 Dank Je Dankscheen спасибо Mauruuru Biyan Diolch i Chi Matondo Terima Kasih Taiku Tack

Blagodaram Ngiyabonga Dziekuje Chokrane Arigato Grazie Mochchakkeram

Juspaxar Gracias Khap Paldies Tingki Gratias Tibi

நன்றி Ua Tsaug Rau Koj Bedankt Dakujem धन्यवाद

Grazas Mesi

Děkuji Nirringrazzjak Hvala Welalin Di Ou Mesi

Suksama Rahmat Matur Nuwun 谢谢 xBala Danke Dhanyavadagalu

Misaotra Matur Nuwun 谢谢 xBala Danke Dhanyavadagalu

Merci Go Raibh Maith Agat Eskerrik Asko

Salamat បាទបាទ អរគុណ

Kia Ora Kop Khun Khap Djiere Dieuf Najis Tuke

ありがとう